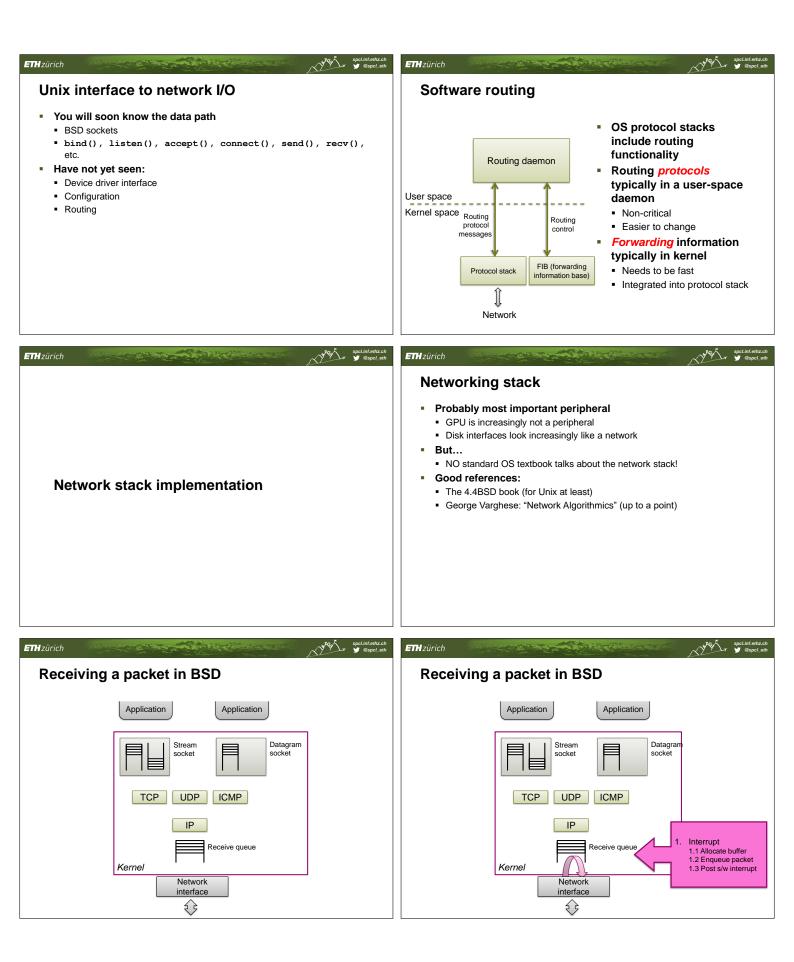
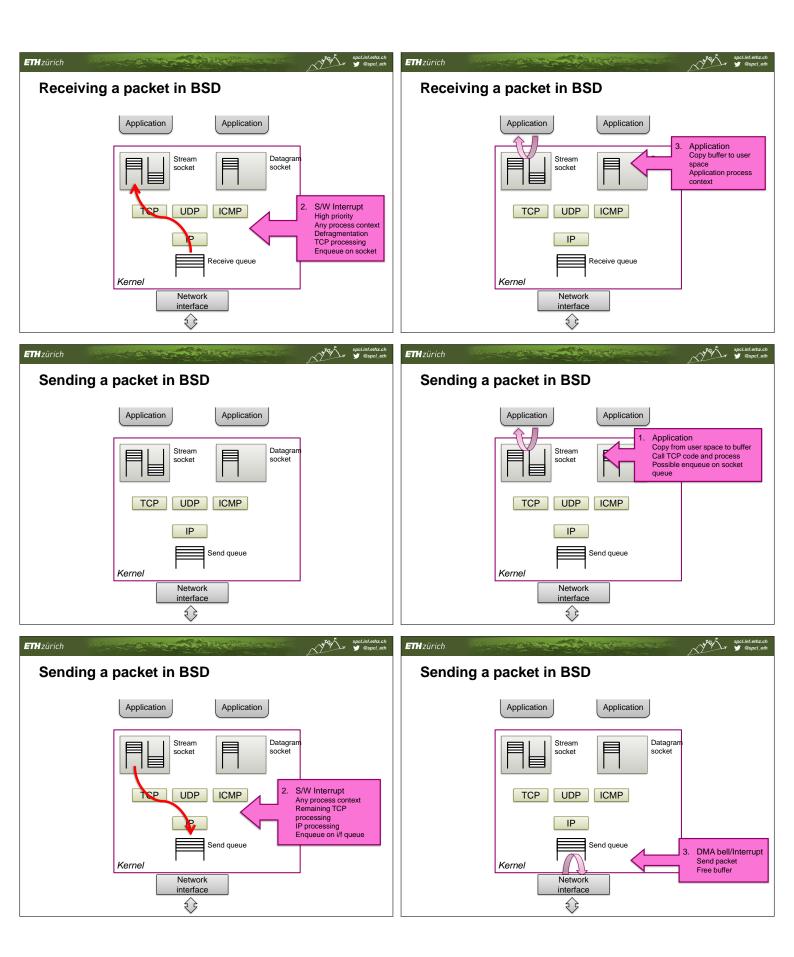
				Administrivia
Networks and Operating Syst	tems (252-	0062-00)		Administra
Chapter 10: I/O Subsystems (-	Contractor Car	If you're an exchange student
	OS Version	Unlock lock Escal screen privile		 and very far away from Zurich during the exam period
EAKING FULL-DISK ENCRYPTION USING FIREWIRE	Windows 8 8.0 Windows 7 SP1	Yes Yes Yes Yes	Yes	 and you still want to take the exam
e have been a number of proof-of-concept hacks using IEEE1394 devices' DMA to elevate eges on a host machine.	Windows 7 SP0 Windows SP2	Yes Yes	Yes	The second balance of the state balance balance is the state of the st
nost useful application of this technique is breaking into machines that use full-disk encryption. there is a tool that will run from any Unix-Like host (Linux, OSX) and can unlock Windows	Windows SP1 Vieta	Yes Yes	Yes	 Then read: <u>https://www.ethz.ch/students/en/studies/performance</u> assessments/exchange.html
ista,7,8,0SX 10.6,10.7,10.8, Ubuntu on both x86 and x64 hosts.	Vitta SP0 Vitta SP0 Windows XP SP3	Yes Yes Yes Yes	Yes	<u>ussessments/exertaingentim</u>
Inception is a FireWire physical memory manipulation and hacking tool exploiting IEEE 1394 SBP-2 DMA. The tool can unlock (any	Windows XP 5P2 Windows XP 5P1	Yes Yes	Yes	Constraints/boundary conditions:
password accepted) and escalate privileges to Administrator/root on almost any machine you have physical access to.	Mar OSX Maverides	Yes(1) Yes(Yes(1) Yes(Yes(1) Yes(1)	It has to be a written exam for fairness – same conditions for all students
It is primarily intended to do its magic against computers that utilize	Mae OS X Lion	Yes(1) Yes(Yes (1)	
full disk encryption such as BitLocker, FileVault, TrueCrypt or Pointsec. There are plenty of other (and better) ways to hack a	Mas OSX Leepard	Yes Yes	Yes	Thus, what we offer is:
machine that doesn't pack encryption. Inception is also useful for incident response teams and digital forensics experts when faced with	Uburta Raring Uburta Quantal	Yes Yes	Yes	We cannot offer preponement (cannot create 10+ different written exams)
live machines. BE CAREFUL WITH I/O DEVICES!	Uburts Precise Uburts Oneiric	Yes Yes Yes Yes	Yes Yes	 But you can take the exam remotely At the exact same time as in Zurich as coordinated by the office above
	Uburta Maverisk Uburta Lacid	Yes (2) Yes (2 Yes (2) Yes (Yes Yes	Only in extreme cases, ETH may be able to move the time!
	Linux Mint 10 Linux Mint 12	Ves Ves	Yes Yes	
zürich		North .	spcl.inf.ethz.ch 💓 @spcl_eth	ETH zürich
Our Small Quiz				
 True or false (raise hand) 				
 Open files are part of the process' address-s 	space			
 Unified buffer caches improve the access tim 	nes significant	ly		
 A partition table can unify the view of multiple 	e disks			
		tions		
 Unix enables to bind arbitrary file systems to 	arbitrary loca	10115		
 The virtual file system interface improves more 	-			
The virtual file system interface improves moProgrammed I/O is efficient for the CPUs	odularity of OS	code		The I/O subsystem
 The virtual file system interface improves mo Programmed I/O is efficient for the CPUs DMA enables devices to access virtual mem 	odularity of OS	code		The I/O subsystem
 The virtual file system interface improves mo Programmed I/O is efficient for the CPUs DMA enables devices to access virtual mem IOMMUs enable memory protection for device 	odularity of OS ory of process ces	code		The I/O subsystem
 The virtual file system interface improves mo Programmed I/O is efficient for the CPUs DMA enables devices to access virtual mem IOMMUs enable memory protection for devic IOMMUs improve memory access performant 	odularity of OS lory of process ces nce	code ses	tware	The I/O subsystem
 The virtual file system interface improves mo Programmed I/O is efficient for the CPUs DMA enables devices to access virtual mem IOMMUs enable memory protection for devic IOMMUs improve memory access performar First level interrupt handlers process the who 	odularity of OS ory of process ces nce ole request fro	code ses	dware	The I/O subsystem
 The virtual file system interface improves mo Programmed I/O is efficient for the CPUs DMA enables devices to access virtual mem IOMMUs enable memory protection for devic IOMMUs improve memory access performant 	odularity of OS ory of process ces nce ole request fro essing latency	code es m the har	lware	The I/O subsystem
 The virtual file system interface improves mo Programmed I/O is efficient for the CPUs DMA enables devices to access virtual mem IOMMUs enable memory protection for devic IOMMUs improve memory access performar First level interrupt handlers process the who Software interrupts reduce the request process 	odularity of OS ory of process ces nce ole request fro essing latency	code es m the har	tware	The I/O subsystem
 The virtual file system interface improves mo Programmed I/O is efficient for the CPUs DMA enables devices to access virtual mem IOMMUs enable memory protection for devic IOMMUs improve memory access performar First level interrupt handlers process the who Software interrupts reduce the request process 	odularity of OS ory of process ces nce ole request fro essing latency	code es m the har	lware	The I/O subsystem
 The virtual file system interface improves mo Programmed I/O is efficient for the CPUs DMA enables devices to access virtual mem IOMMUs enable memory protection for devic IOMMUs improve memory access performar First level interrupt handlers process the who Software interrupts reduce the request process 	odularity of OS ory of process ces nce ole request fro essing latency	code es m the har	lware	The I/O subsystem
 The virtual file system interface improves mo Programmed I/O is efficient for the CPUs DMA enables devices to access virtual mem IOMMUs enable memory protection for devic IOMMUs improve memory access performar First level interrupt handlers process the who Software interrupts reduce the request proce Deferred procedure calls execute second-leve 	odularity of OS ory of process ces nce ole request fro essing latency	code es m the har	dware spci.int.ethz.ch ¥ ®spci.eth	The I/O subsystem
 The virtual file system interface improves models of the virtual file system interface improves models of the virtual memels of the virtual memel	odularity of OS ory of process ces nce ole request fro essing latency	code es m the har	3 spcl.inf.ethz.ch	
 The virtual file system interface improves models of the virtual file system interface improves models of the virtual memels of the virtual m	odularity of OS nory of process ces nce ble request fro essing latency vel interrupt ha	m the hard	S spci.inf.ethz.ch ¥®spci.eth	ETHzürich The I/O Subsystem
The virtual file system interface improves models of the comparation of the comparat	odularity of OS nory of process ces nce ble request fro essing latency vel interrupt ha	m the hard	S spci.inf.ethz.ch ¥®spci.eth	ETHzürich The I/O Subsystem • Caching - fast memory holding copy of data
The virtual file system interface improves models Programmed I/O is efficient for the CPUs DMA enables devices to access virtual memels IOMMUs enable memory protection for device IOMMUs improve memory access performance First level interrupt handlers process the whole Software interrupts reduce the request proced Deferred procedure calls execute second-leve zürich Generic I/O functionality	odularity of OS nory of process ces nce ble request fro essing latency vel interrupt ha	m the hard	S spci.inf.ethz.ch ¥®spci.eth	ETHzürich The I/O Subsystem
 The virtual file system interface improves models of the programmed I/O is efficient for the CPUs DMA enables devices to access virtual memels IOMMUs enable memory protection for device IOMMUs improve memory access performant First level interrupt handlers process the whote software interrupts reduce the request proces Deferred procedure calls execute second-level 	odularity of OS nory of process ces nce ble request fro essing latency vel interrupt ha	m the hard	S spci.inf.ethz.ch ¥®spci.eth	ETHzürich The I/O Subsystem • Caching - fast memory holding copy of data • Always just a copy • Key to performance
 The virtual file system interface improves modeled in the virtual file system interface improves modeled in the Programmed I/O is efficient for the CPUs DMA enables devices to access virtual memeled in the Programmed I/O is efficient for the CPUs DMA enables devices to access virtual memeled in the Programmed I/O is efficient for the CPUs DMMUs improve memory protection for device in the Programmed I/O is efficient for the Programmer for the Programmer First level interrupt handlers process the whote Software interrupts reduce the request proceed. Deferred procedure calls execute second-level procedure calls execute second-level proceedure calls execute second level proceedure cal	odularity of OS ory of process ces nce ble request fro essing latency vel interrupt ha	andlers	spcl.inf.ethz.ch ♥ @spcl.eth	ETHzürich The I/O Subsystem • Caching - fast memory holding copy of data • Always just a copy • Key to performance • Spooling - hold output for a device
 The virtual file system interface improves modeled in the virtual file system interface improves modeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMMUs improve memory protection for device IOMMUs improve memory protection for device Software interrupt handlers process the whole interrupts reduce the request proced. Deferred procedure calls execute second-leve 	odularity of OS ory of process ces nce ble request fro essing latency vel interrupt ha	andlers	spcl.inf.ethz.ch ♥ @spcl.eth	ETHZürich The I/O Subsystem • Caching - fast memory holding copy of data • Always just a copy • Key to performance • Spooling - hold output for a device • If device can serve only one request at a time
 The virtual file system interface improves modeled in the virtual file system interface improves modeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMMUs improve memory protection for device in the cPUs interrupt handlers process performanely access performa	odularity of OS ory of process ces nce ble request fro essing latency vel interrupt ha	andlers	spcl.inf.ethz.ch ♥ @spcl.eth	ETHzürich The I/O Subsystem • Caching - fast memory holding copy of data • Always just a copy • Key to performance • Spooling - hold output for a device
 The virtual file system interface improves modeled in the virtual file system interface improves modeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMMUs improve memory protection for device IOMMUs improve memory protection for device Software interrupt handlers process the whole interrupts reduce the request proced. Deferred procedure calls execute second-leve 	odularity of OS ory of process ces nce ble request fro essing latency vel interrupt ha	andlers	spcl.inf.ethz.ch ♥ @spcl.eth	ETHZürich The I/O Subsystem • Caching - fast memory holding copy of data • Always just a copy • Key to performance • Spooling - hold output for a device • If device can serve only one request at a time
 The virtual file system interface improves modeled in the virtual file system interface improves modeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMMUs improve memory protection for device IOMMUs improve memory protection for device Software interrupt handlers process the whole interrupts reduce the request proced. Deferred procedure calls execute second-leve 	odularity of OS ory of process ces nce ble request fro essing latency vel interrupt ha	andlers	spcl.inf.ethz.ch ♥ @spcl.eth	ETHZürich The I/O Subsystem • Caching - fast memory holding copy of data • Always just a copy • Key to performance • Spooling - hold output for a device • If device can serve only one request at a time
 The virtual file system interface improves modeled in the virtual file system interface improves modeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMMUs improve memory protection for device in the cPUs interrupt handlers process performanely access performanely access performanely interrupt handlers process the whole interrupts reduce the request process. Deferred procedure calls execute second-levely 	odularity of OS nory of process ces noce ble request fro essing latency vel interrupt ha	andlers	spcl.inf.ethz.ch ♥ @spcl.eth	ETHZürich The I/O Subsystem • Caching - fast memory holding copy of data • Always just a copy • Key to performance • Spooling - hold output for a device • If device can serve only one request at a time
 The virtual file system interface improves modeled in the virtual file system interface improves modeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMMUs improve memory protection for device IOMMUs improve memory protection for device Software interrupt handlers process the whole interrupts reduce the request proced. Deferred procedure calls execute second-leve 	odularity of OS nory of process ces noce ble request fro essing latency vel interrupt ha	andlers	spcl.inf.ethz.ch ♥ @spcl.eth	ETHZürich The I/O Subsystem • Caching - fast memory holding copy of data • Always just a copy • Key to performance • Spooling - hold output for a device • If device can serve only one request at a time
 The virtual file system interface improves modeled in the virtual file system interface improves modeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMMUs improve memory protection for device IOMMUs improve memory protection for device Software interrupt handlers process the whole interrupts reduce the request proced. Deferred procedure calls execute second-leve 	odularity of OS nory of process ces noce ble request fro essing latency vel interrupt ha	andlers	spcl.inf.ethz.ch ♥ @spcl.eth	ETHZürich The I/O Subsystem • Caching - fast memory holding copy of data • Always just a copy • Key to performance • Spooling - hold output for a device • If device can serve only one request at a time
 The virtual file system interface improves modeled in the virtual file system interface improves modeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMA enables devices to access virtual memeled in the cPUs DMMUs improve memory protection for device IOMMUs improve memory protection for device Software interrupt handlers process the whole interrupts reduce the request proced. Deferred procedure calls execute second-leve 	odularity of OS nory of process ces noce ble request fro essing latency vel interrupt ha	andlers	spcl.inf.ethz.ch ♥ @spcl.eth	ETHZürich The I/O Subsystem • Caching - fast memory holding copy of data • Always just a copy • Key to performance • Spooling - hold output for a device • If device can serve only one request at a time
 The virtual file system interface improves modeled in the virtual file system interface improves modeled in the Programmed I/O is efficient for the CPUs DMA enables devices to access virtual memeled in the PMU subscription for device in the PMU subscription for device in the PMU subscription in the PMU subscription for device in the PMU subscription in the PMU subscription in the PMU subscription in the PMU subscription is the PMU subscription in the PMU subscription in the PMU subscription is the PMU subscription in the PMU subscripting subscription in the PMU subscription in the PMU subscripting	odularity of OS nory of process ces noce ble request fro essing latency vel interrupt ha	andlers	spcl.inf.ethz.ch ♥ @spcl.eth	ETHZürich The I/O Subsystem • Caching - fast memory holding copy of data • Always just a copy • Key to performance • Spooling - hold output for a device • If device can serve only one request at a time
 The virtual file system interface improves modeled in the virtual file system interface improves modeled in the Programmed I/O is efficient for the CPUs DMA enables devices to access virtual memeled in the PMU subscription for device in the PMU subscription for device in the PMU subscription in the PMU subscription for device in the PMU subscription in the PMU subscription in the PMU subscription in the PMU subscription is the PMU subscription in the PMU subscription in the PMU subscription is the PMU subscription in the PMU subscripting subscription in the PMU subscription in the PMU subscripting	odularity of OS nory of process ces noce ble request fro essing latency vel interrupt ha	andlers	spcl.inf.ethz.ch ♥ @spcl.eth	ETHZürich The I/O Subsystem • Caching - fast memory holding copy of data • Always just a copy • Key to performance • Spooling - hold output for a device • If device can serve only one request at a time

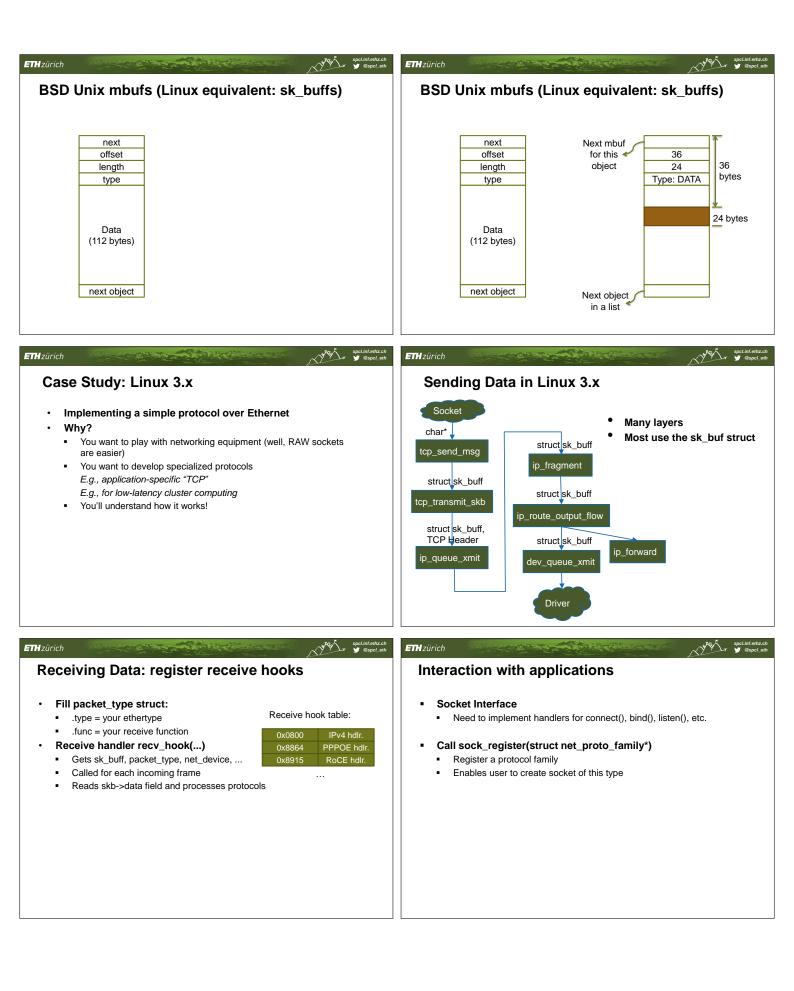
zürich spol.inteinz. ✓ 🖉 Spol. (seh eth ETH zürich section v sectio
The I/O Subsystem	Naming and Discovery
 Scheduling Some I/O request ordering via per-device queue Some OSs try fairness Buffering - store data in memory while transferring between devices or memory To cope with device speed mismatch To cope with device transfer size mismatch To maintain "copy semantics" 	 What are the devices the OS needs to manage? Discovery (bus enumeration) Hotplug / unplug events Resource allocation (e.g., PCI BAR programming) How to match driver code to devices? Driver instance ≠ driver module One driver typically manages many models of device How to name devices inside the kernel? How to name devices outside the kernel?
¶zürich spelinteihz. ♥ @spel.e	eth EIHzurich
Matching drivers to devices	Naming devices in the Unix kernel
 Devices have unique (model) identifiers E.g., PCI vendor/device identifiers Drivers recognize particular identifiers Typically a list Kernel offers a device to each driver in turn Driver can "claim" a device it can handle Creates driver instance for it. 	 (Actually, naming device driver instances) Kernel creates identifiers for Block devices Character devices [Network devices - see later] Major device number: Class of device (e.g., disk, CD-ROM, keyboard) Minor device number: Specific device within a class
Hzürich	ch eth ETHzürich
Unix Block Devices	Character Devices
 Used for "structured I/O" Deal in large "blocks" of data at a time Often look like files (seekable, mappable) Often use Unix' shared buffer cache Mountable: File systems implemented above block devices 	 Used for "unstructured I/O" Byte-stream interface – no block boundaries Single character or short strings get/put Buffering implemented by libraries Examples: Keyboards, serial lines, mice Distinction with block devices somewhat arbitrary

ETH zürich	ETH zürich y Spolintethz.eh
Mid-lecture mini-quiz	Naming devices outside the kernel
 Character or block device (raise hand) Console USB stick Microphone Screen (graphics adapter) Network drive Webcam 	 Device files: special type of file Inode encodes <type, major="" minor="" num="" num,=""></type,> Created with mknod() system call Devices are traditionally put in /dev /dev/sda - First SCSI/SATA/SAS disk /dev/sda5 - Fifth partition on the above /dev/cdrom0 - First DVD-ROM drive /dev/ttyS1 - Second UART
ETH zürich SpcLinlethzeh	ETH zürich Spelint.eth.zeh
Pseudo-devices in Unix	Old-style Unix device configuration
 Devices with no hardware! Still have major/minor device numbers. Examples: /dev/stdin (was a device earlier, now link) /dev/kmem /dev/random /dev/loop0 	 All drivers compiled into the kernel Each driver probes for any supported devices System administrator populates /dev Manually types mknod when a new device is purchased! Pseudo devices similarly hard-wired in kernel
ETH zürich Spel.intebiz.ch	ETH zürich
 Linux device configuration today Physical hardware configuration readable from /sys Special fake file system: sysfs Plug events delivered by a special socket Drivers dynamically loaded as kernel modules Initial list given at boot time User-space daemon can load more if required /dev populated dynamically by udev User-space daemon which polls /sys 	Interface to network I/O

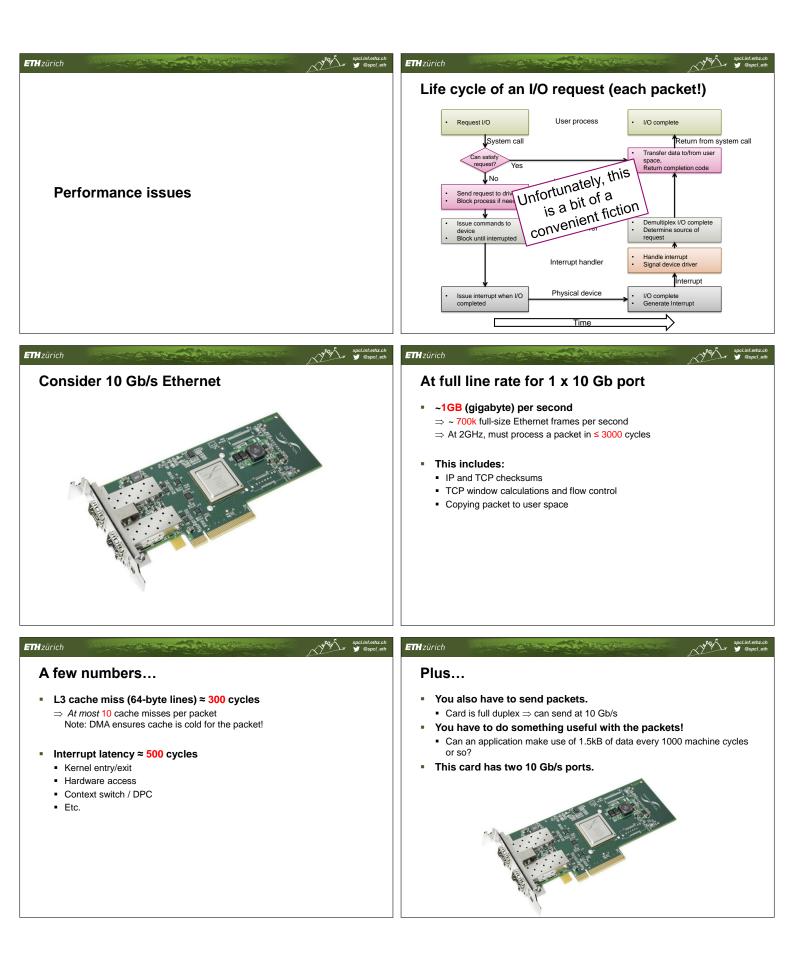




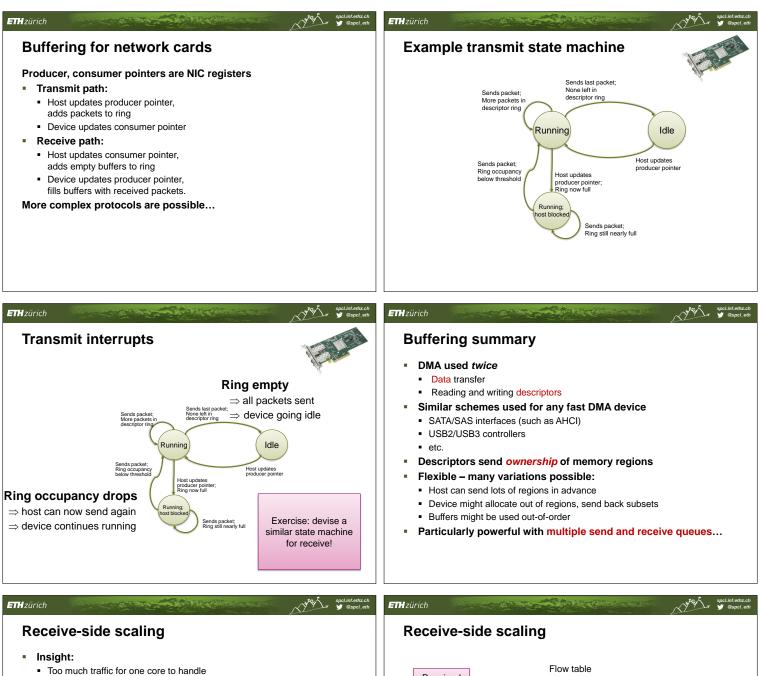
ETH zürich spelint eth zch 🔮 spel eth	ETH zürich
The TCP state machine	OS TCP state machine
Closed Active open / SYN Passive Open Close SYN / SYNACK SYN / SYNACK SYN / SYNACK SYN / SYNACK SYN / SYNACK Close / FIN ACK FIN_wait_2 FIN / ACK FIN / ACK Close / FIN Close / Close / FIN Close / FIN Close / Close / Close / Close / Close / FIN Close / Close /	 More complex! Also needs to handle: Congestion control state (window, slow start, etc.) Flow control (window size) Retransmission timeouts Etc. State transitions triggered when: User request: send, recv, connect, close Packet arrives Timer expires Actions include: Set or cancel a timer Enqueue a packet on the transmit queue Enqueue a packet on the socket receive queue Create or destroy a <i>TCP control block</i>
ETH zürich	ETH zürich
In-kernel protocol graph	Protocol graphs
e.g. Tunneling e.thermet device	 Graph nodes can be: Per-protocol (handle all flows) Packets are "tagged" with demux tags Per-connection (instantiated dynamically) Multiple interfaces as well as connections Ethernet ↔ Ethernet ⇒ bridging IP ↔ IP ⇒ IP routing
ETH zürich spclimt.eth.zch	ETH zürich Spelini.eth zw
	Memory management
Memory management	 Problem: how to ship packet data around Need a data structure that can: Easily add, remove headers Avoid copying lots of payload Uniformly refer to half-defined packets Fragment large datasets into smaller units Solution: Data is held in a linked list of "buffer structures" Destimation: ETH IP TCP User Data ETH



ETH zürich	ETH zürich Spol.Int.eth.zoh y @spol.eth
 Anatomy of struct sk_buff Called "skb" in Linux jargon Allocate via alloc_skb() (or dev_alloc_skb() if in driver) Free with kfree_skb() (dev_kfree_skb()) Use pskb_may_pull(skb, len) to check if data is available skb_pull(skb, len) to advance the data pointer <i>it even has a webpage! http://www.skbuff.net/</i> 	 SKB fields Double-linked list, each skb has .next/.prev .data contains payload (size of data field is set by skb_alloc) .sk is the socket this skb is owned by .mac_header, .network_header, .transport_header contain headers of various layers .dev is the device this skb uses 58 member fields total
ETH zürich	ETH zürich Spel.inf.eth.zch Spel.inf.eth.zch Spel.inf.eth.zch
Case study: IP fragmenting	Case study: IP fragmenting
 Linux <2.0.32: // Determine the position of this fragment. Two fragments: end = offset + iph->tot_len - ihl; 100 100 // Check for overlap with preceding fragment, and, if needed, // align things so that any overlaps are eliminated. Offset: 0 if (prev != NULL && offset < prev->end) { i = prev->end - offset; offset 100 // Initialize segment structure fp->offset 100 // Initialize segment structure fp->offset = offset; fp->end = end; fp->end = offset; fp->end = end; fp->end = offset; fp->end = end; fp->end = offset; for the end; fp->end = end; fp->end = end; fp->end = end; fp->end = fp->len) { error_to_big; } memcpy((ptr + fp->offset), fp->ptr, fp->len); count += fp->next; 	 Linux <2.0.32: // Determine the position of this fragment. Two fragments: end = offset + iph->tot_len - ihl; 100 30 #1 // Check for overlap with preceding fragment, and, if needed, // align things so that any overlaps are eliminated. Offset: 0 if (prev != NULL && offset < prev->end) { i = prev->end - offset; offset += i; /* ptr into datagram */ ptr += i; /* ptr into fragment data */ 100 #2 // Check for overlap with preceding fragment, and, if needed, // align things so that any overlaps are eliminated. I Length: 100 Unitialize segment structure offset; fp->end = end; fp->end = end; fp->end = end; fp->end = end; fp->len = end - offset; for -ro = 4294967226 0 100 10 30 if (count-fp->len > skb->len) { error_to_big; } memcpy((ptr + fp->offset), fp->ptr, fp->len); count += fp->len; fp = fp->next;
ETH zürich spol.ml.eth.zch ♥ @spol.eth	ETH zürich Spol Int.eth.zoh y @spol eth
Case study: IP fragmenting	2.0.32 that's so last century!
Hindows A fatal exception 0E has occurred at 0028:C1891963 in UXD ctpci9x(05) • 00001853. The current application will be terminated. * Press any key to terminate the current application. * Press any key to terminate the current applications. Bress any unsaved information in all applications. Press any key to continue _	Beauty TechCenter > Searchy Butletins > Marroant Security Butletin M309-050 Control C



ETH zürich yel.ht.eth.zch y @@spcl.eth	ETH zürich 🖉 espel aft
<section-header>And Plus • And if you thought that was fast • Mellanox FDR 200 Gb/s Adapter • Mellanox FDR 200 Gb/s Adapter • Impossible to use without advanced features <i>RDMA</i> SR-IOV TOE Interrupt coalescing</section-header>	 What to do? TCP offload (TOE) Put TCP processing into hardware on the card Buffering Transfer lots of packets in a single transaction Interrupt coalescing / throttling Don't interrupt on every packet Don't interrupt at all if load is very high Receive-side scaling Parallelize: direct interrupts and data to different cores
ETHzürich Spellnteitiz.eh	ETH zürich
 Mitigate interrupt pressure Each packet interrupts the CPU vs. CPU polls driver NAPI switches between the two! NAPI-compliant drivers Offer a poll() function Which calls back into the receive path 	 Driver enables polling with netif_rx_schedule(struct net_device *dev) Disables interrupts Driver deactivates polling with netif_rx_complete(struct net_device *dev) Re-enable interrupts. → but where does the data go???
ETH zürich spci intethzch y @spci eth	ETH zürich
 Buffering Key ideas: Decouple sending and receiving Neither side should wait for the other Only use interrupts to unblock host Batch requests together Spread cost of transfer over several packets 	Producer-consumer buffer descriptor rings



- Cores aren't getting any faster
- ⇒ Must parallelize across cores
- Key idea: handle different flows on different cores
 - But: how to determine flow for each packet?
 - Can't do this on a core: same problem!
- Solution: demultiplex on the NIC
- DMA packets to per-flow buffers / queuesSend interrupt only to core handling flow

Flow table pointer +IP src + dest +TCP src + dest Etc. Hash of packet header DMA Core to address interrupt

ETH zürich	ETH zürich spel.int.eth.zch ♥ @spel.eth
Receive-side scaling	Next (final) week
 Can balance flows across cores Note: doesn't help with one big flow! Assumes: n cores processing <i>m</i> flows is faster than one core Hence: Network stack and protocol graph must <i>scale</i> on a multiprocessor. Multiprocessor scaling: topic for later 	 Virtual machines Multiprocessor operating systems