

Design of Parallel and High-Performance Computing

Fall 2015

Lecture: Organization of the Course

Instructor: Torsten Hoefler & Markus Püschel

TA: Timo Schneider

ETH

Eidgenössische Technische Hochschule Zürich
Swiss Federal Institute of Technology Zurich

The Team

Professors: Torsten Höfler & Markus Püschel

TA: Timo Schneider



Guest lecturer: we'll see

Possibly consultants for projects

Course website: <http://spcl.inf.ethz.ch/Teaching/2015-dphpc/>

2

Administrative

- **Lecture: Mo 13:15 – 16:00**
- **Recitation: Do 13:15 – 15:00**
 - Takes place as announced on website
 - Sometimes used as lecture or swapped with lecture
 - Used for project updates
- **Help:**
 - Email Timo: timo.schneider@inf.ethz.ch
 - Or do you prefer office hours?

3

Administrative

- **Website:** <http://spcl.inf.ethz.ch/Teaching/2015-dphpc/>
- **Will contain all material (slides, homeworks, schedule, etc.)**
- **Mailing list:** <https://spcl.inf.ethz.ch/cgi-bin/mailman/listinfo/dphpc15>
- **Background material:**
 - Maurice Herlihy and Nir Shavit: The Art of Multiprocessor Programming. Morgan Kaufmann, 2012
 - Papers as mentioned

4

Work and Grading

- **Work during semester:**
 - Regular homeworks
 - Project
- **Grade:**
 - 50% Project
 - 50% Written exam (120 minutes)

5

Project

- **Teams of 3 (look for partners now)**
- **Topic that fits the course material**
 - More later (this Thursday)
 - You are encouraged to choose a topic
- **Milestones**
 - Pick topic: in about a month
 - Project progress presentations: about a month before end
 - Project presentations: last week of class
- **Report:**
 - Due around mid January
 - 6 pages, conference style
 - Template provided

6

Course Name

- Design of Parallel and High-Performance Computing
- Design of Parallel and High-Performance Computing Platforms?
- Design of Parallel and High-Performance Computing Applications?
- Design of Parallel and High-Performance Computing Systems?

- Design of Parallel and High-Performance Computing:
Understand principal issues involved in software development for parallel computing