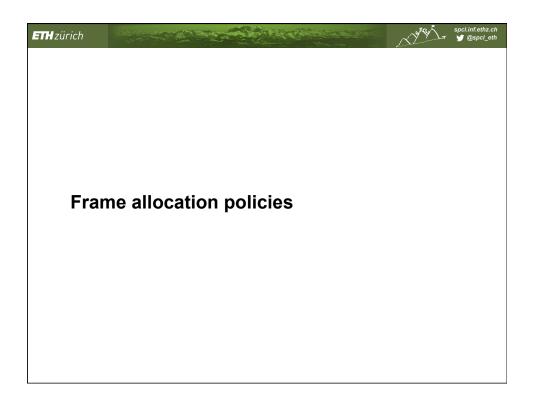
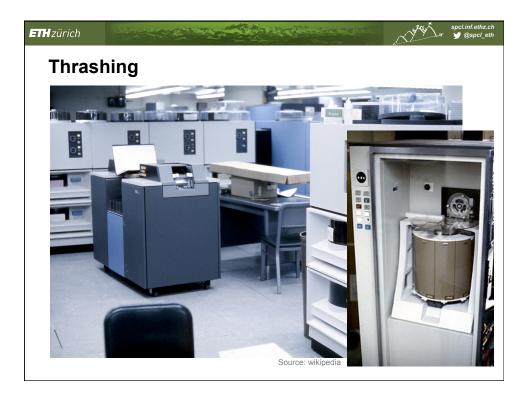
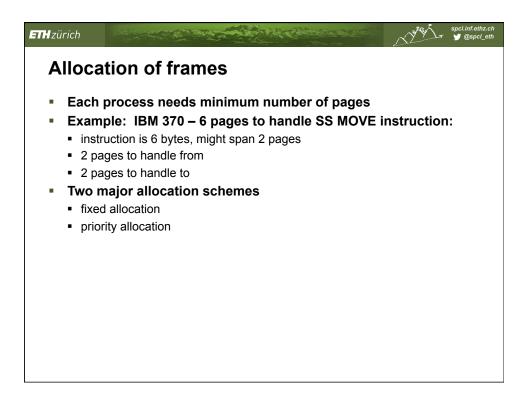


ETH zürich Spci.i Paging OS back in ... **Base + limit registers** Uses for virtual memory Segmentation Copy-on-write Paging **Demand paging** Page fault handling Page protection Page replacement algorithms Page sharing • ... Page table structures TLB shootdown

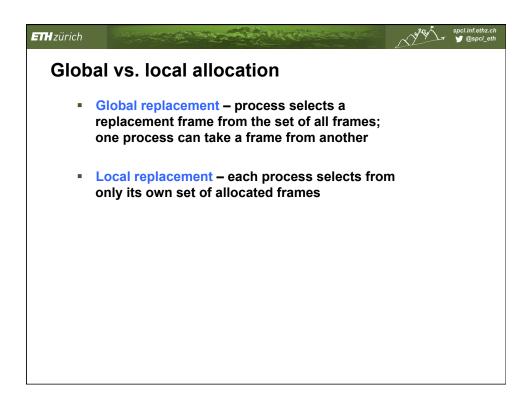
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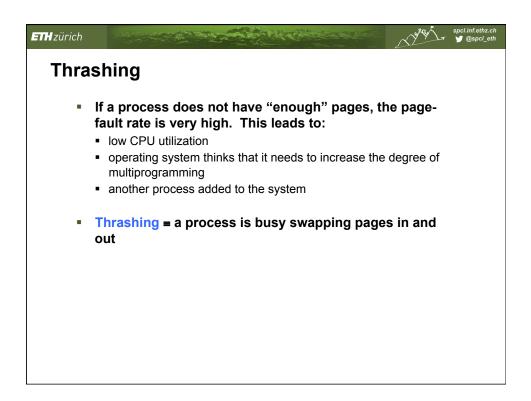


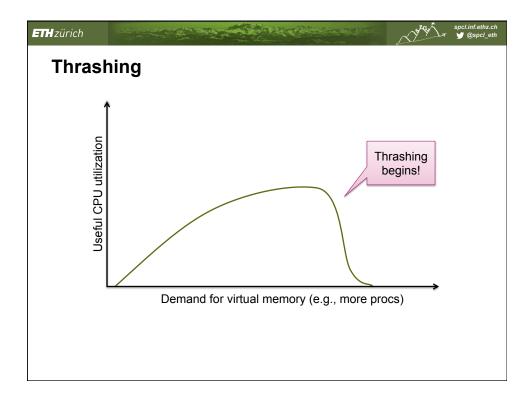


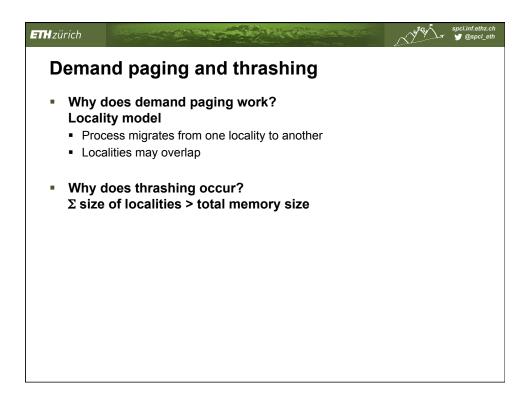
ETHzürich	spcl.inf.ethz.ch ¥®spcl_eth
Fixed allocation	
 Equal allocation all processes get equal share Proportional allocation allocate according to the size of proce 	255
$s_{i} = \text{size of process } p_{i}$ $S = \sum s_{i}$ $m = \text{total number of frames}$ $a_{i} = \text{allocation for } p_{i} = \frac{s_{i}}{S} \times m$	m = 64 $s_1 = 10$ $s_2 = 127$ $a_1 = \frac{10}{137} \times 64 \approx 5$ $a_2 = \frac{127}{137} \times 64 \approx 59$

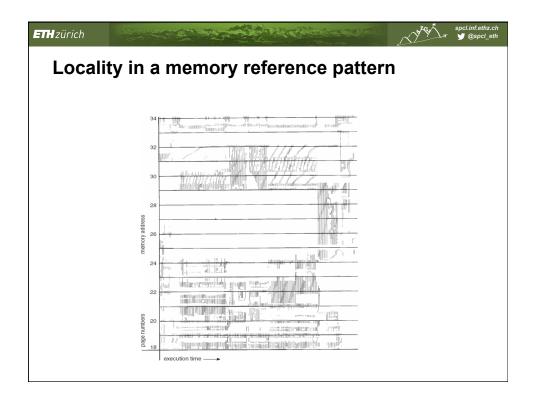


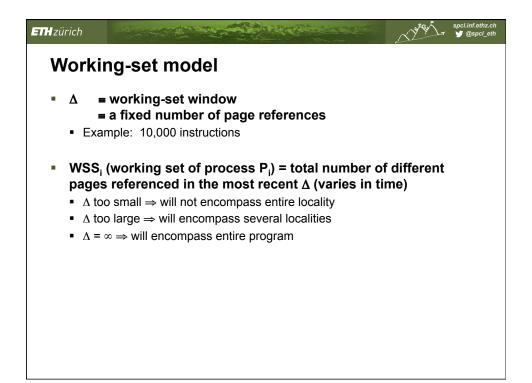
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Priorit	y allocation		
-	ortional allocation scheme priorities rather than size		
1.	cess P_i generates a page fault, select: one of its frames, or frame from a process with lower priority		



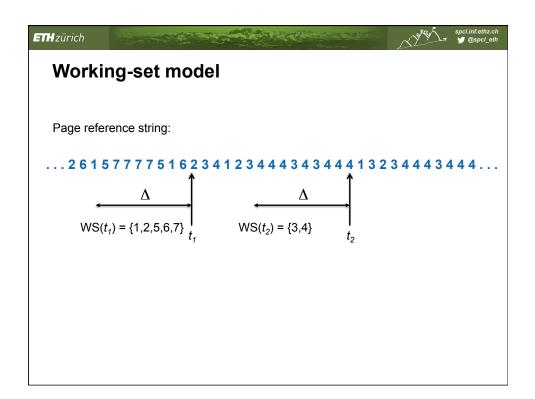




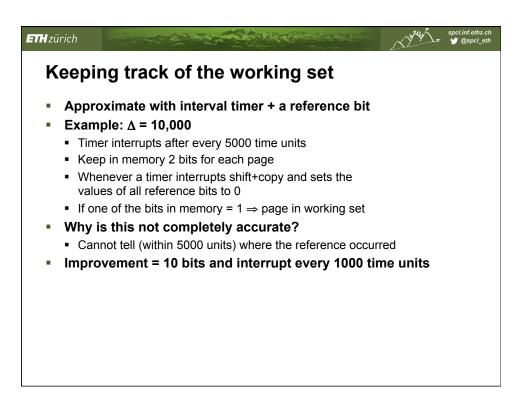


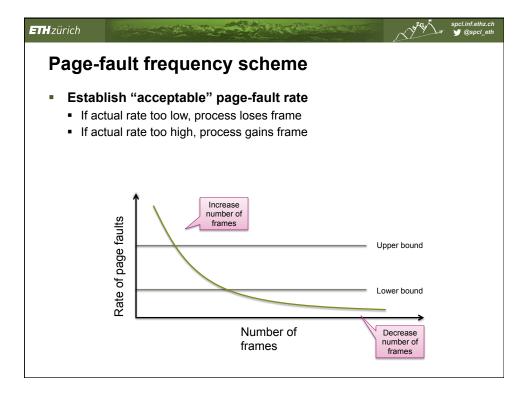


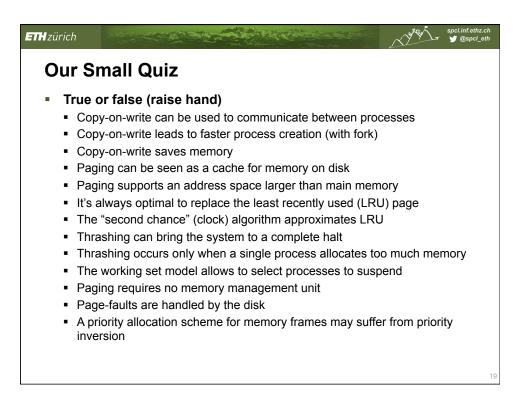
ETHzürich	Marger -	spcl.inf.ethz.ch Ƴ @spcl_eth
Allocate demand frames		
 D = Σ WSS_i = total demand frames Intuition: how much space is really needed 		
■ D > m ⇒ Thrashing		
 Policy: if D > m, suspend some processes 		

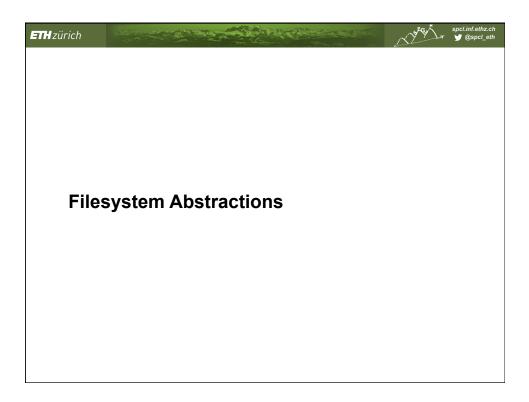


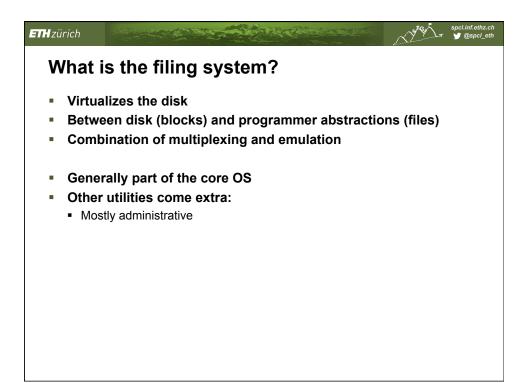
 Example: Δ = 10,000 Timer interrupts after every 5000 time units Keep in memory 2 bits for each page Whenever a timer interrupts shift+copy and sets the values of all reference bits to 0 If one of the bits in memory = 1 ⇒ page in working set 	
 Keep in memory 2 bits for each page Whenever a timer interrupts shift+copy and sets the values of all reference bits to 0 If one of the bits in memory = 1 ⇒ page in working set 	
 Whenever a timer interrupts shift+copy and sets the values of all reference bits to 0 If one of the bits in memory = 1 ⇒ page in working set 	
 values of all reference bits to 0 If one of the bits in memory = 1 ⇒ page in working set 	
Why is this not completely converte?	
Why is this not completely accurate?	
 Hint: Nyquist-Shannon! 	



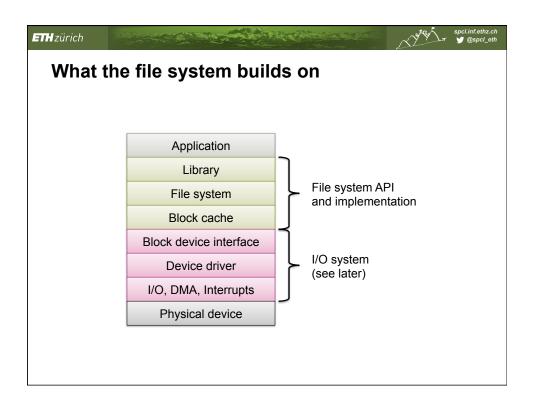


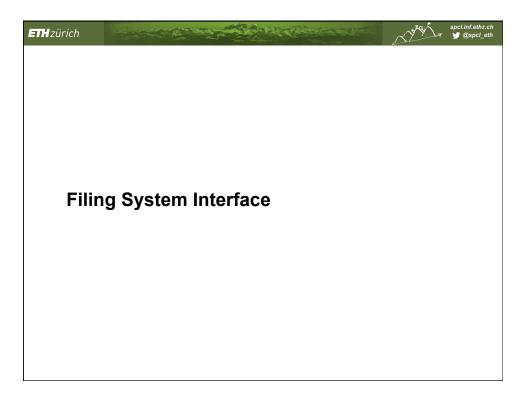


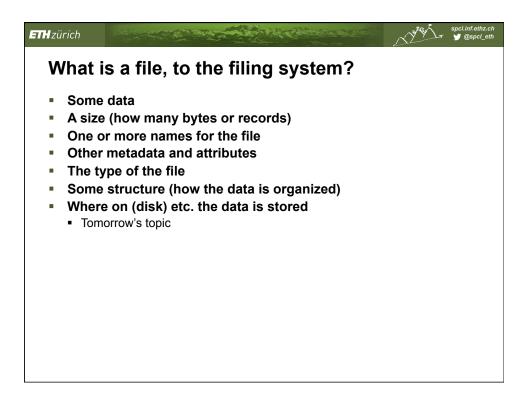




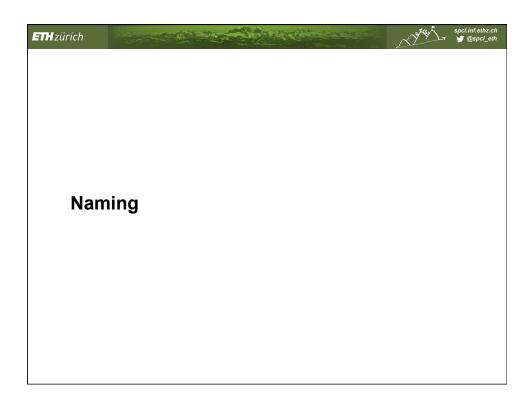
What does the file system need to provide?			
Goal	Physical characteristic	Design implication	
High performance	High cost of I/O access	Organize placement: access data in large, sequential units Use caching to reduce I/O	
Named data	Large capacity, persistent across crashes, shared between programs	Support files and directories with meaningful names	
Controlled sharing	Device stores many users' data	Include access control metadata with files	
Reliable storage	Crashes occur during update	Transactions to make set of updates atomic	
	Storage devices fail	Redundancy to detect and correct failures	
	Flash memory wears out	Wear-levelling to prolong life	

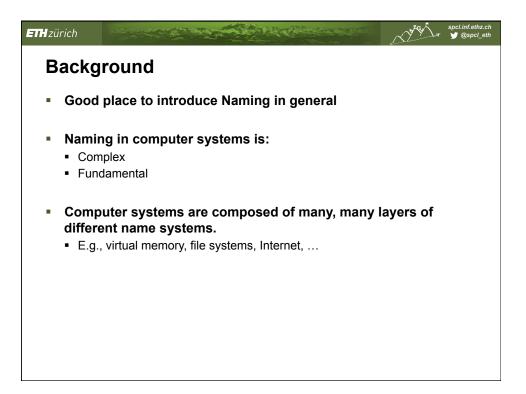


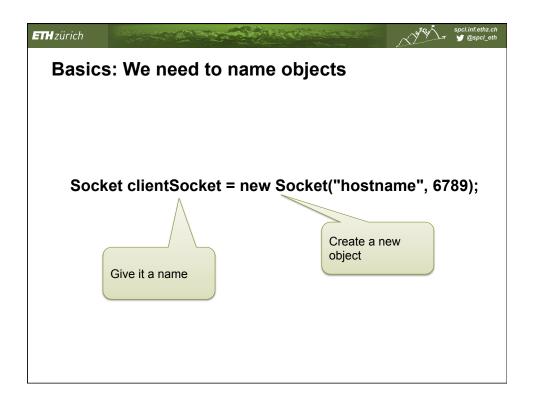


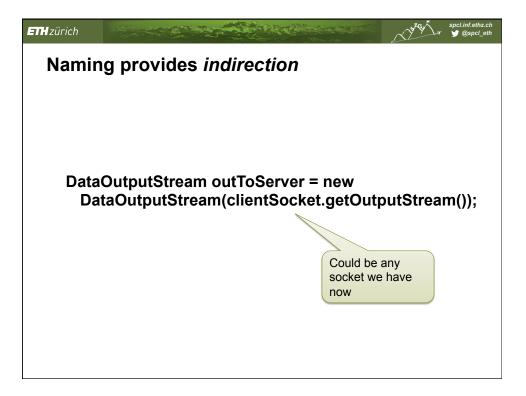


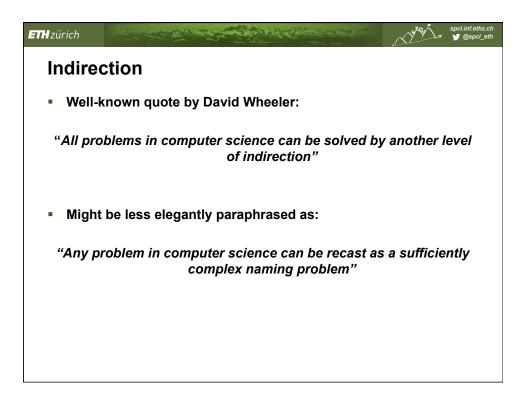
ETHzürich	A Start	spcl.inf.ethz.ch ਤ 🍯 @spcl_eth
File m	netadata	
 Date File r Nate Loc Tim Ow File 	data: important concept! ta about an object, not the object itself metadata examples: me cation on disk (next lecture) nes of creation, last change, last access nership, access control rights (perhaps) e type, file structure (later) hitrary descriptive data (used for searching)	

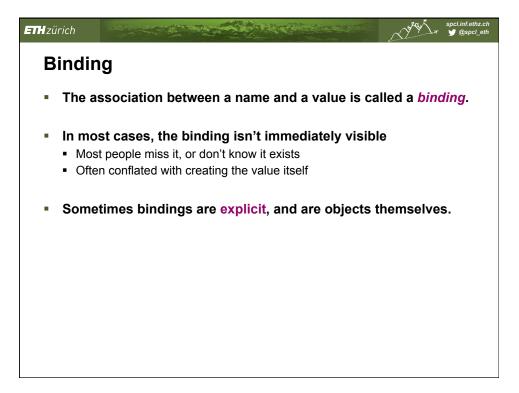


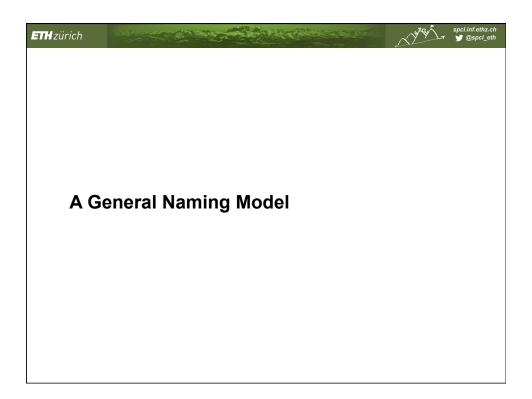




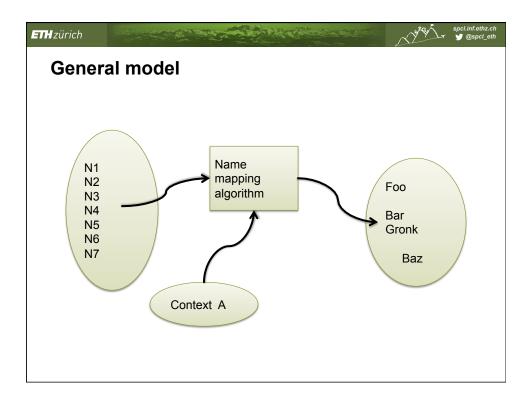


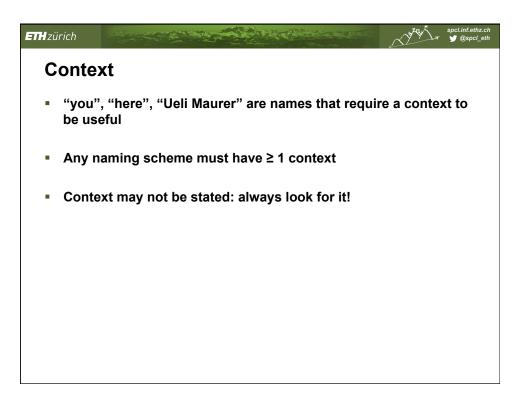


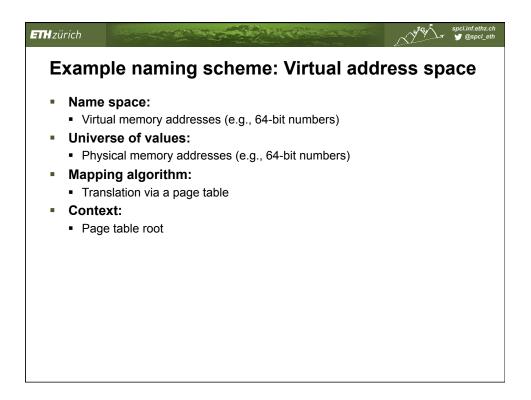




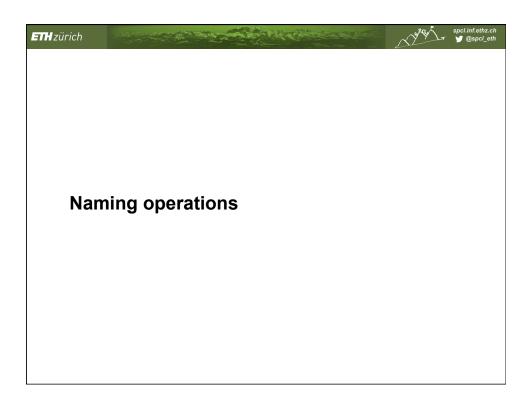
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A ge	neral model of naming
• Des 1. 2. 3.	igner creates a naming scheme. Name space: what names are valid? Universe of values: what values are valid? Name mapping algorithm: what is the association of names to values?
• Map	oping algorithm also known as a resolver
Req	uires a <i>context</i>



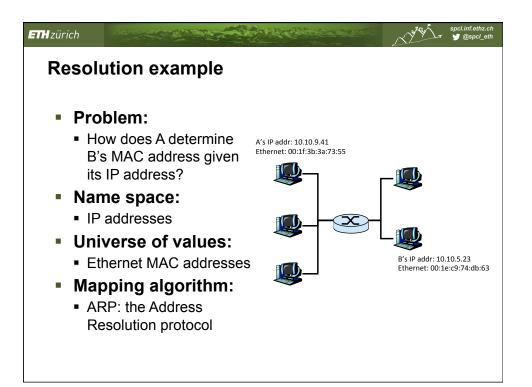




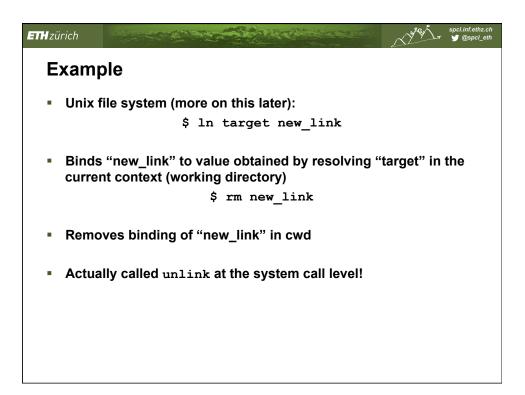
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Single vs. multiple contexts	
 IPv4 addresses: E.g., 129.132.102.54 Single (global) context: routable from anywhere Well, sort of 	
 ATM virtual circuit/path identifiers E.g., 43:4435 Local context: only valid on a particular link/port Many contexts! 	

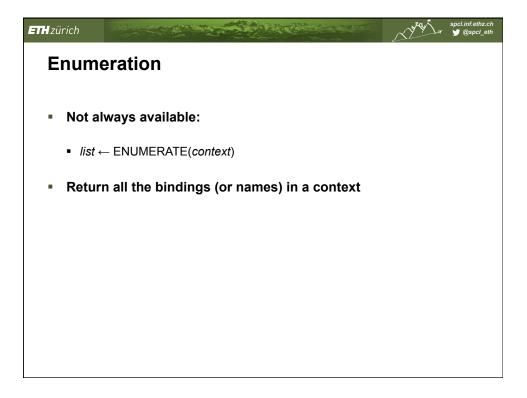


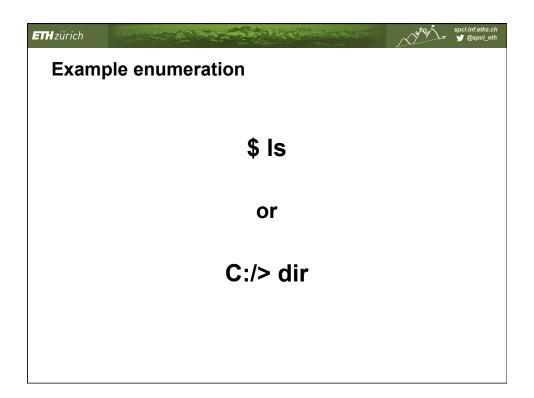
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Resolu	ution
 Basic 	operation:
• valu	$e \leftarrow RESOLVE(name, context)$
 In pra 	ctice, resolution mechanism depends on context:
■ valu	$e \leftarrow context.RESOLVE(name)$

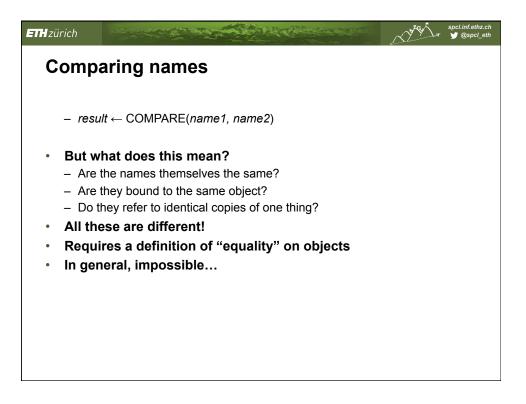


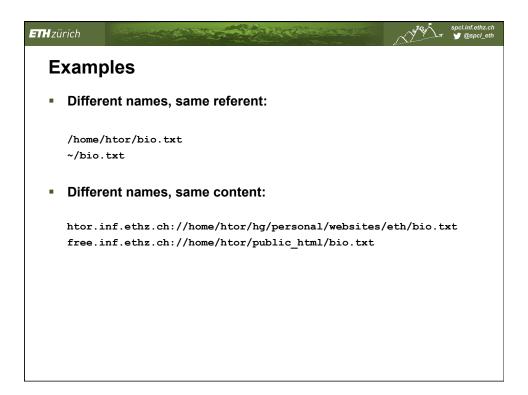
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Managing bindings	
 Typical operations: 	
 status ← BIND(name, value, context) status ← UNBIND(name, context) 	
 May fail according to naming scheme rules Unbind may need a value 	

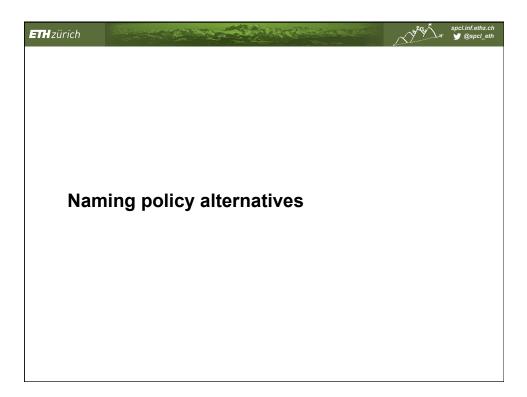


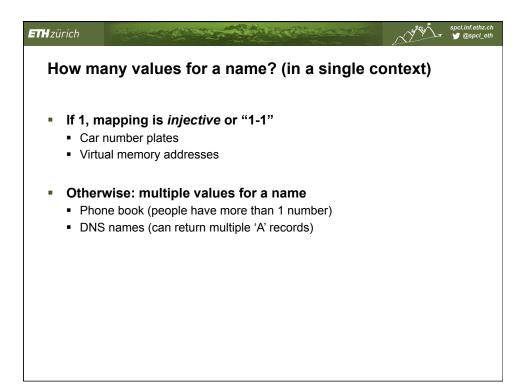




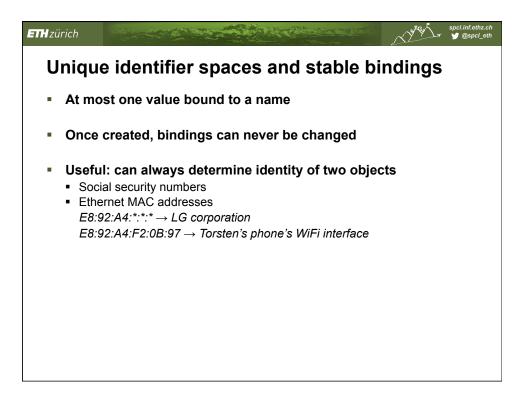


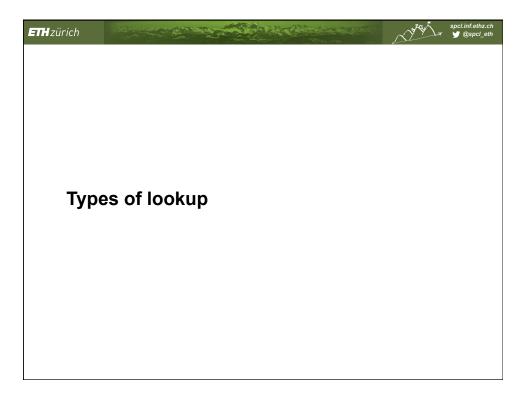


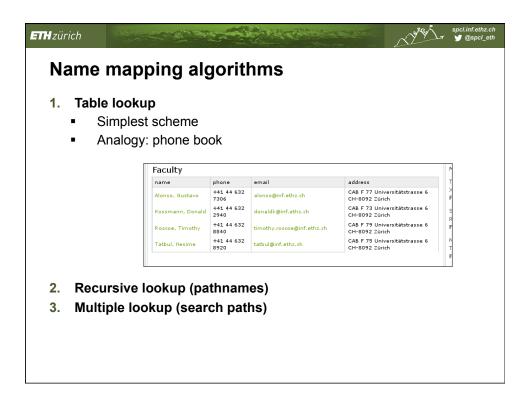


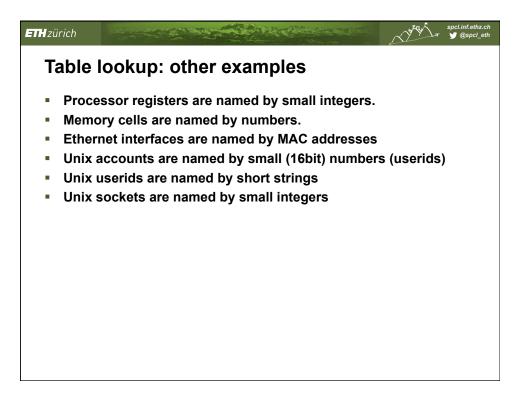


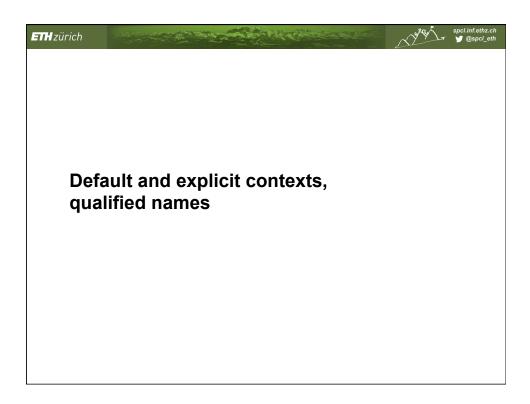
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How many names for	a value?		
 Only one name for each val Names of models of car IP protocol identifiers 	ue		
 Multiple names for the same Phone book again (people shate) URLs (multiple links to same particular stress of the same particular	aring a home phone)		



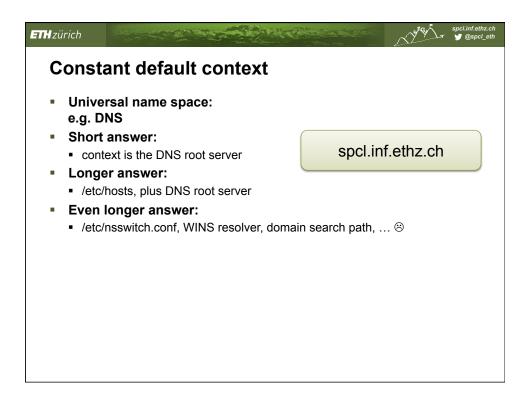




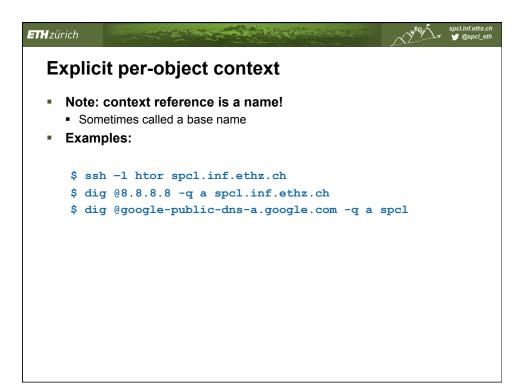




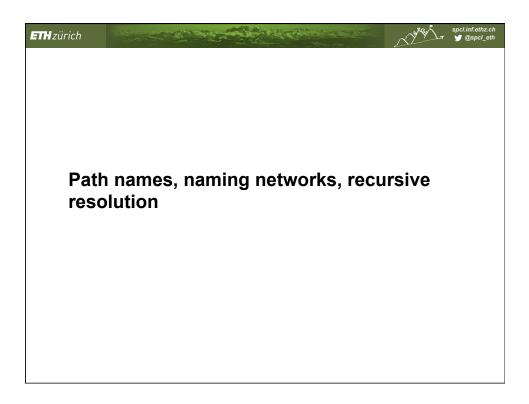
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W	here is the context?		
1.	 Default (implicit): supplied by the resolver Constant: built in to the resolver Variable: from current environment (state) 		
2.	Explicit: supplied by the object1. Per object2. Per name (qualified name)		



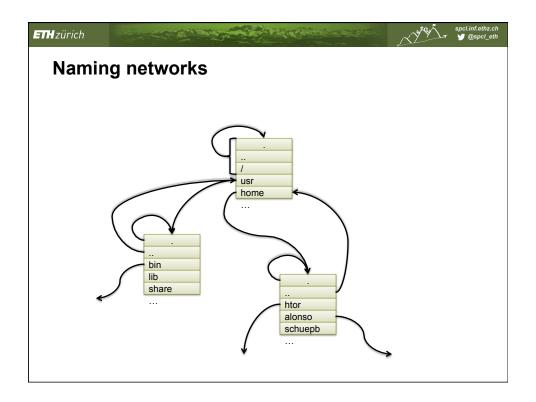
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Variable de	efault co	ontext			
Example: c	urrent work	king directo	ry		
<pre>\$ pwd /home/htor/ \$ ls osnet/ \$ cd osnet \$ ls archive/ assignments \$ ls lectur chapter1/ chapter10/ chapter11/ \$</pre>	lecture/ / legis/ e chapter2/ chapter3/	chapter5/ chapter6/	sessions/ chapter8/	svnadmin/ svn-commit.tm template.pptx	φ

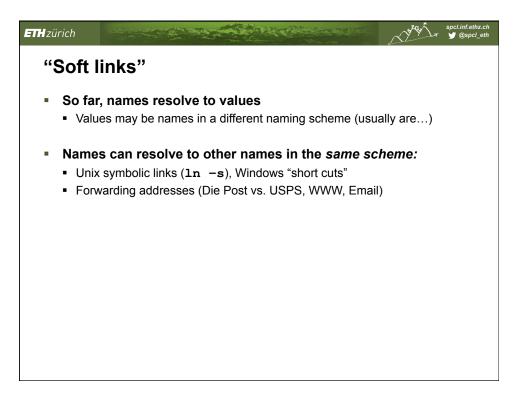


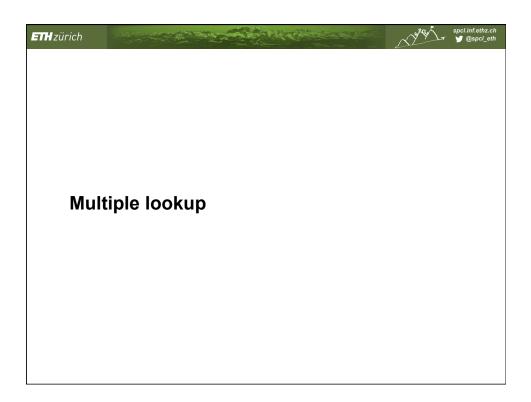
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Explicit per-name context		
 Each name comes with its context Actually, the <i>name</i> of the context (context,name) = qualified name 		
 Recursive resolution process: Resolve <i>context</i> to a context object Resolve <i>name</i> relative to resulting context 		
 Examples: <u>htor@inf.ethz.ch</u> /var/log/syslog 		



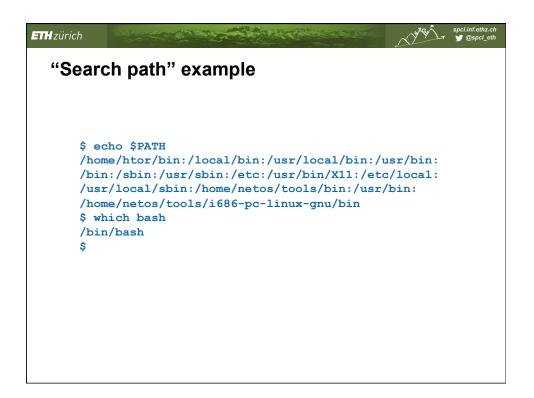
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Path names	
 Recursive resolution ⇒ path names 	
 Name can be written forwards or backwards – Examples: /var/log/messages or spcl.inf.ethz.ch 	
 Recursion must terminate: Either at a fixed, known context reference (the root) Or at another name, naming a default context Example: relative pathnames 	
 Syntax gives clue (leading '/') Or trailing "." as in spcl.inf.ethz.ch. 	

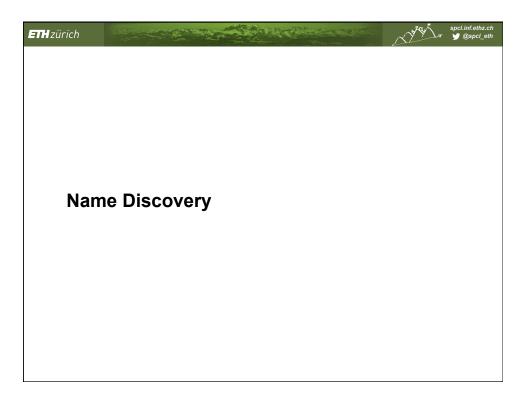


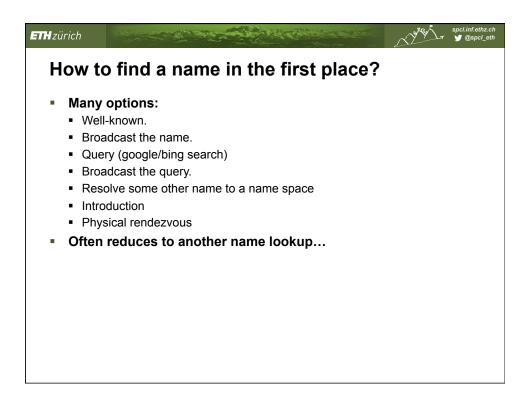


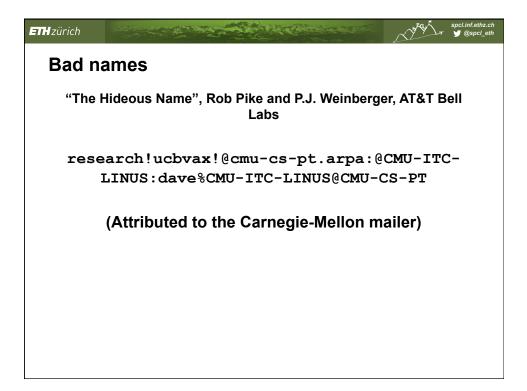


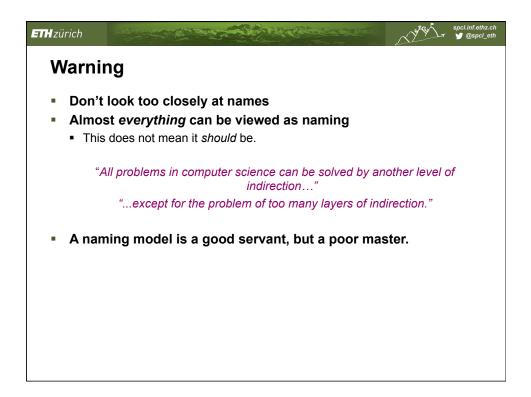
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Some	times, one context is not end	ough
 try s Union Exam bina reso 	ple lookup, or "search path" several contexts in order n mounts: overlay two or more contexts nples: ary directories in Unix olving symbols in link libraries ewhat controversial	
Note:	: "search", but not in the Google sense.	



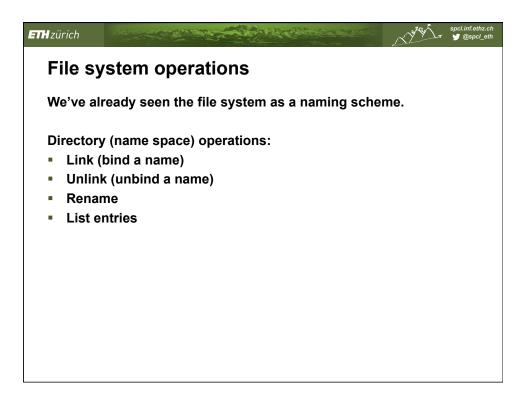


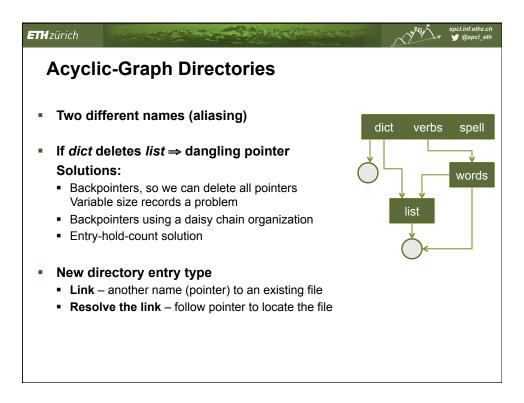


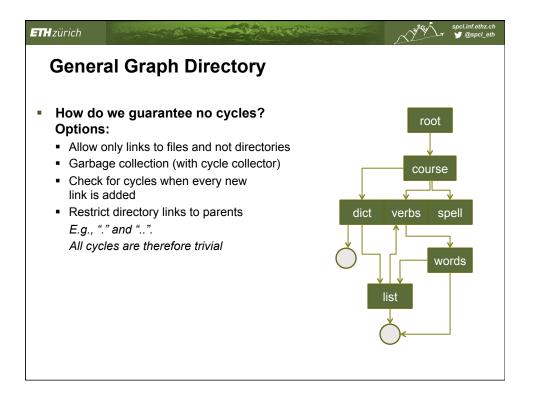


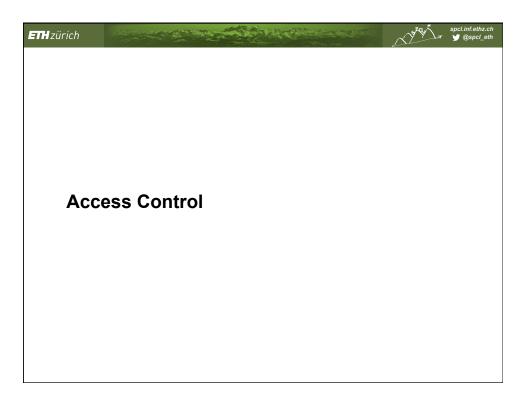


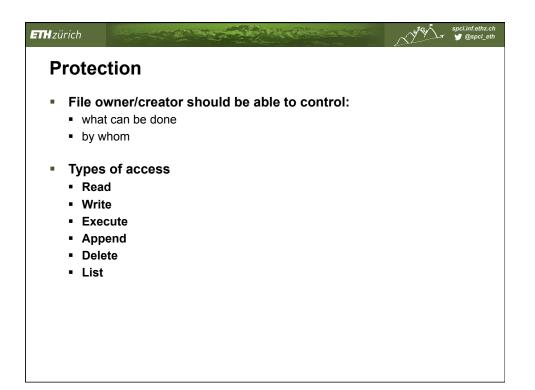
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Concl	usion	
NamConRes	ing is everywhere in Computer Systems me spaces ntexts solution mechanisms n understanding a system, ask:	
WhatWhatWhatWhen	at are the naming schemes? at's the context? at's the policy? n designing a system, it <i>will</i> help stop you making (mistakes!	some)



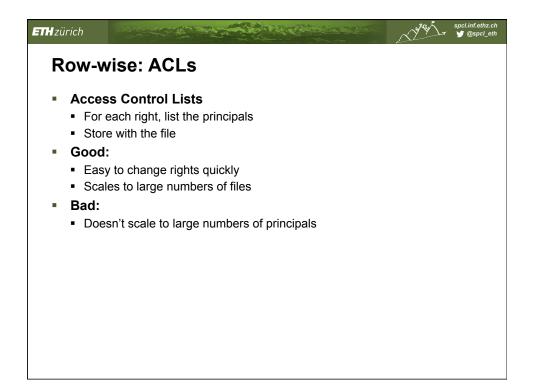




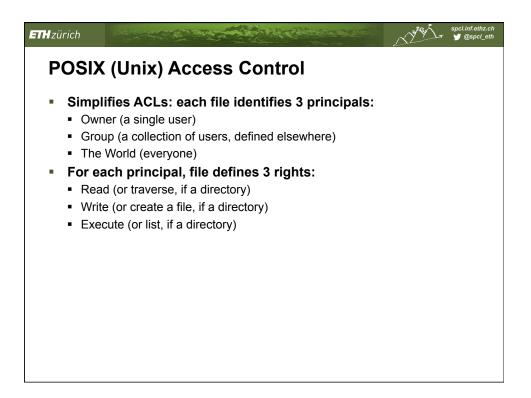




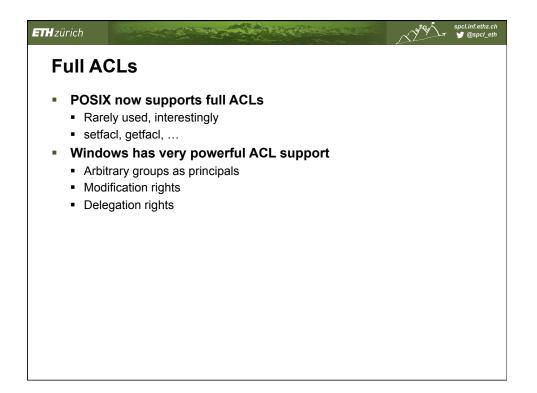
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Acces	s contr	ol	ma	trix										
For a sir	ngle file or c	lirect	ory:											
				F	Princi	pals								
		Α	В	С	D	Е	F	G	Н	J				
	Read	\checkmark	Ø	Ø			\checkmark	Ø						
ts	Write	\checkmark	\checkmark		\checkmark			\checkmark						
Rights	Append	Ø				Ø								
Ľ.	Execute	\square	\square	Ø	\square									
	Delete	Ø												
	List	\checkmark				\checkmark								
		Pro	oblen	n: ho	w to :	scala	bly re	epres	ent tl	nis m	atrix	?		

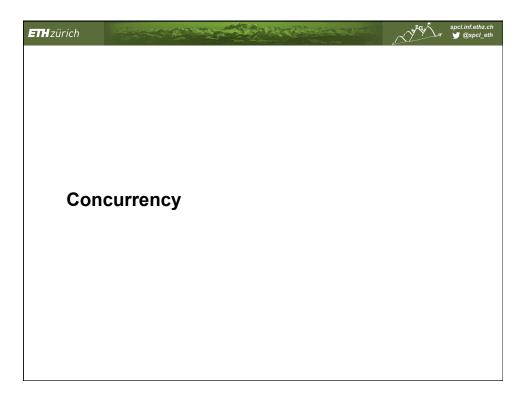


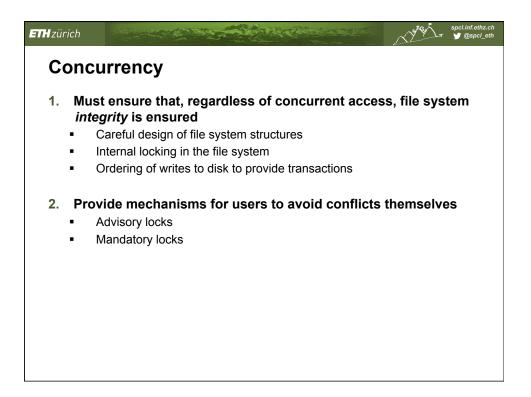
ETHzürich	spcl.inf.ethz.ch
Column-wise: Capabilities	
 Each principal with a right on a file h right 	olds a <i>capability</i> for that
Stored with principal, not object (file)Cannot be forged or (sometimes) copied	
 Good: Very flexible, highly scalable in principals Access control resources charged to prin 	
 Bad: Revocation: hard to change access rights (need to keep track of who has what capate) 	



ETHzürich	spcl.inf.ethz.ch ✔
Exam	ple
	<pre>PIC Invxxx 9 htor htor 4096 May 9 13:14 pagai total 860 invxxx 3 htor htor 4096 Jan 29 15:58 autoconf invxxx 4 htor htor 4096 Jan 29 15:58 autoconf invxxx 4 htor htor 4096 Jan 29 15:57 configure invxxx 4 htor htor 16401 bec 25 13:20 CMakelists.txt rw 1 htor htor 16401 bec 25 13:20 CMakelists.txt rw 1 htor htor 2782 Jan 29 15:57 configure rw 1 htor htor 4096 Jan 29 15:57 configure rw 1 htor htor 4096 Jan 29 15:57 configure rw 1 htor htor 16401 bec 25 13:20 CMakelists.txt rw 1 htor htor 4096 Jan 29 15:57 configure rw 1 htor htor 4096 Dec 25 13:20 CMakelists.txt rw 1 htor htor 4096 Dec 25 13:20 configure rw 1 htor htor 4096 Dec 25 13:20 cranples frwxx.x 10 htor htor 4096 Dec 25 13:20 include frwxx.x 18 htor htor 4096 Dec 25 13:20 include frwx 1 htor htor 1855 Dec 25 13:20 LLVMBuild txt rw 1 htor htor 1855 Dec 25 13:20 Makefile.common rw 1 htor htor 1259 Dec 25 13:20 Makefile.common rw 1 htor htor 4096 Dec 25 13:20 Inv.Mseifile.common rw 1 htor htor 4096 Dec 25 13:20 Trest frwxx.x 4 htor htor 4096 Dec 25 13:20 Trest frwxx.x 4 htor htor 4096 Dec 25 13:20 Fabelies frwxx.x 4 htor htor 4096 Dec 25 13:20 Fabelies frwxx.x 4 htor htor 4096 Dec 25 13:20 Trest frwxx.x 4 htor htor 4096 Dec 25 13:20 Fabelies frwxx.x 4 htor htor 4096 Dec 25 13:21 for bis frwxx.x 3 faber htor 4096 Dec 25 13:21 for bis frwxx.x 3 faber htor 4096 Dec 25 13:21 for bis frwxx.x 4 htor htor 4096 Dec 25 13:21 for bis frwxx.x</pre>
	frwxxx 32 htor htor 4096 Jan 29 15:57 utils







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Com	mon locking facilities		
	e: dvisory: separate locking facility andatory: write/read operations will fail		
• Gra • •	nularity: Whole-file Byte ranges (or record ranges) Write-protecting executing binaries		

