Design of Parallel and High Performance Computing
HS 2014
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Homework 7
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Locks and Lock-Free

Exercise 1

Implement the following locks in C for the x86 architecture: TAS, TAS+Backoff, TATAS.

Exercise 2

Implement a parallel stack datastructure in C. Use a single lock to provide mutual exclusion when accessing the stack from multiple threads. Use the above three locks from your implementation and compare the timings.