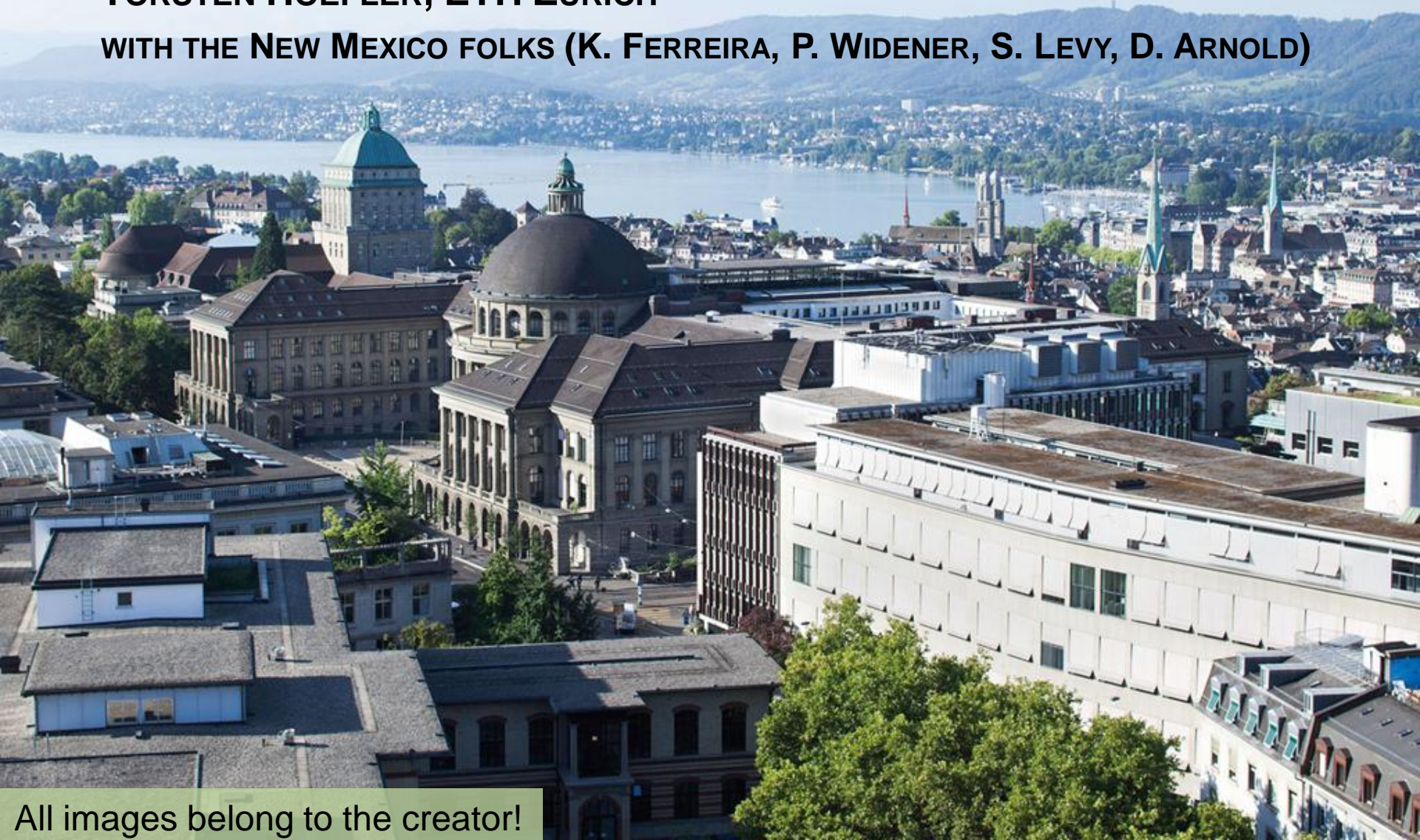


# RESILIENCE OVERHEADS AT SCALE AND SCALABILITY

TORSTEN HOEFLER, ETH ZURICH

WITH THE NEW MEXICO FOLKS (K. FERREIRA, P. WIDENER, S. LEVY, D. ARNOLD)



# Fault-tolerance Interfaces

- **Very simple**

- Coordinated Checkpointing

*void take\_coordinated\_checkpoint(void \*data, int size, char\* output)*

- Uncoordinated Checkpointing

*void take\_uncoordinated\_checkpoint(void \*data, int size, char\* output)*

- **But complex to use**

- Which option? Coordinated, uncoordinated?
- Where to write files to (HD, SSD, parallel FS)?

# Overall Goal of the Project: Exascale Analysis

- **Evaluate overall scalability of resilience techniques**
  - For very large scale systems [PMBS'13]
- **Offer a freely available framework for reproducible work**
  - Provide traces for key DOE workloads [trace repo]
  - Enables cross-validation of results [LSAP'10]
- **Evaluate scalability of uncoordinated checkpoint/restart (uCR) for DOE workloads [SC14]**
  - Identify issues
  - Investigate solutions
    - Clustered checkpointing [SC14]*
    - Nonblocking collectives [EuroMPI'14]*

[LSAP'10]: TH, Schneider, Lumsdaine: LogGOPSIm - Simulating Large-Scale Applications in the LogGOPS Model

[PMBS'13]: Widener, Ferreira, Levy, Hoefler: Exploring the effect of noise on the performance benefit of nonblocking allreduce

[SC14]: Ferreira, Widener, Levy, Arnold, TH: Understanding the Effects of Communication and Coordination on Checkpointing at Scale

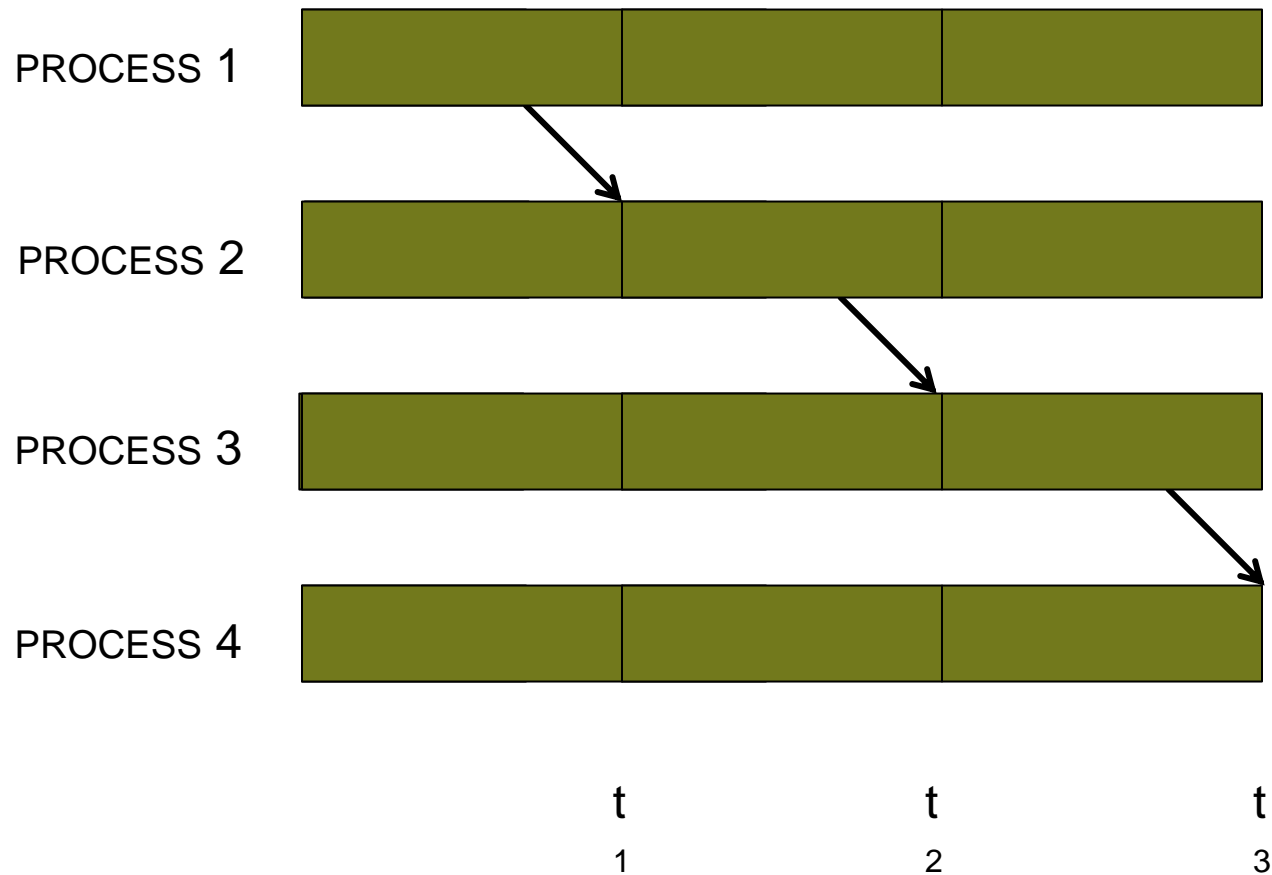
[trace repo]: <http://hstor.inf.ethz.ch:8888/>



# Take Away Messages

- **The effect of happens-before delay chains:**
  1. Local checkpoints can have a greater performance impact than message logging overheads for uCR;
  2. An application's communication pattern dictates whether uCR checkpoint overheads are amplified or absorbed;
  3. Collective communication limits the extent to which the execution run-ahead of surviving processes actually improves overall application execution time.
  
- **Mitigation strategies:**
  1. Checkpoint clustering protocols can be used to improve uCR performance
  2. Nonblocking collective communication
  
- **Reproducing results: LogGOPSim [LSAP'10,online]**

# Resilience Today: Coordinated Checkpoint/Restart (cCR)



# Coordinated Checkpoint/Restart

- **Dominance due to a number of key assumptions**
  - Some of which may continue to hold true for future systems:

*Failure that do not crash the system (SDC) are rare*  
*Checkpoints used for other purposes (i.e , steering, viz)*

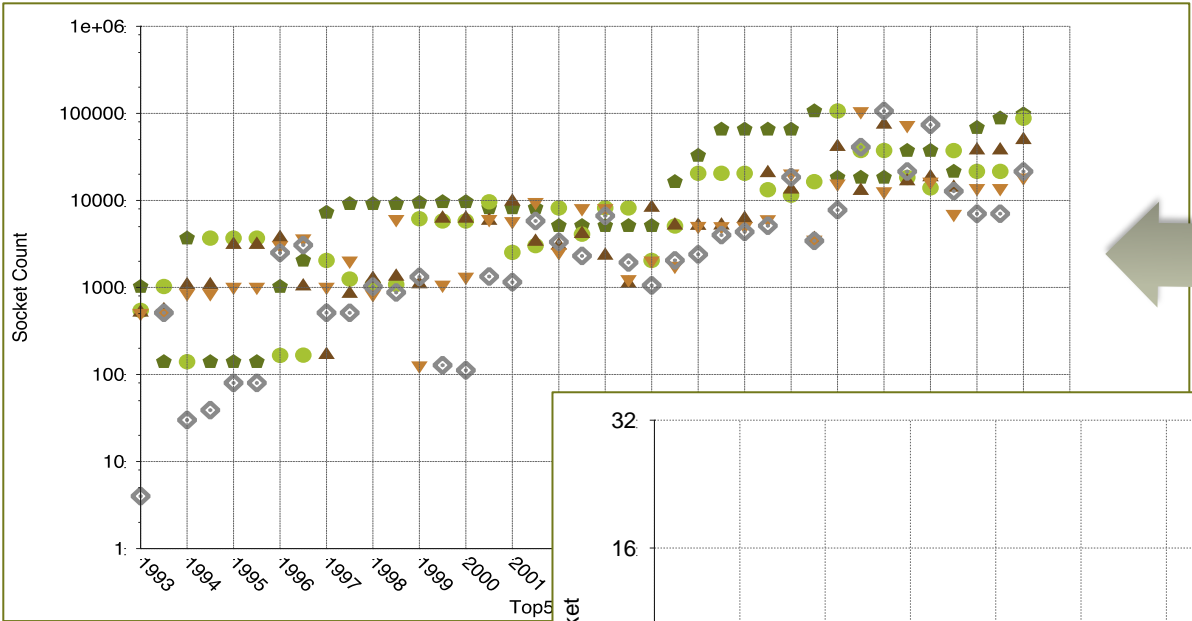


- Some of which may not:

*Application state can be saved and restored much more quickly than a system's mean time to interrupt (MTTI)*  
*The hardware and upkeep (e.g., power) costs of supporting frequent checkpointing is a modest portion (currently perhaps 10-20%) of the system's overall cost*

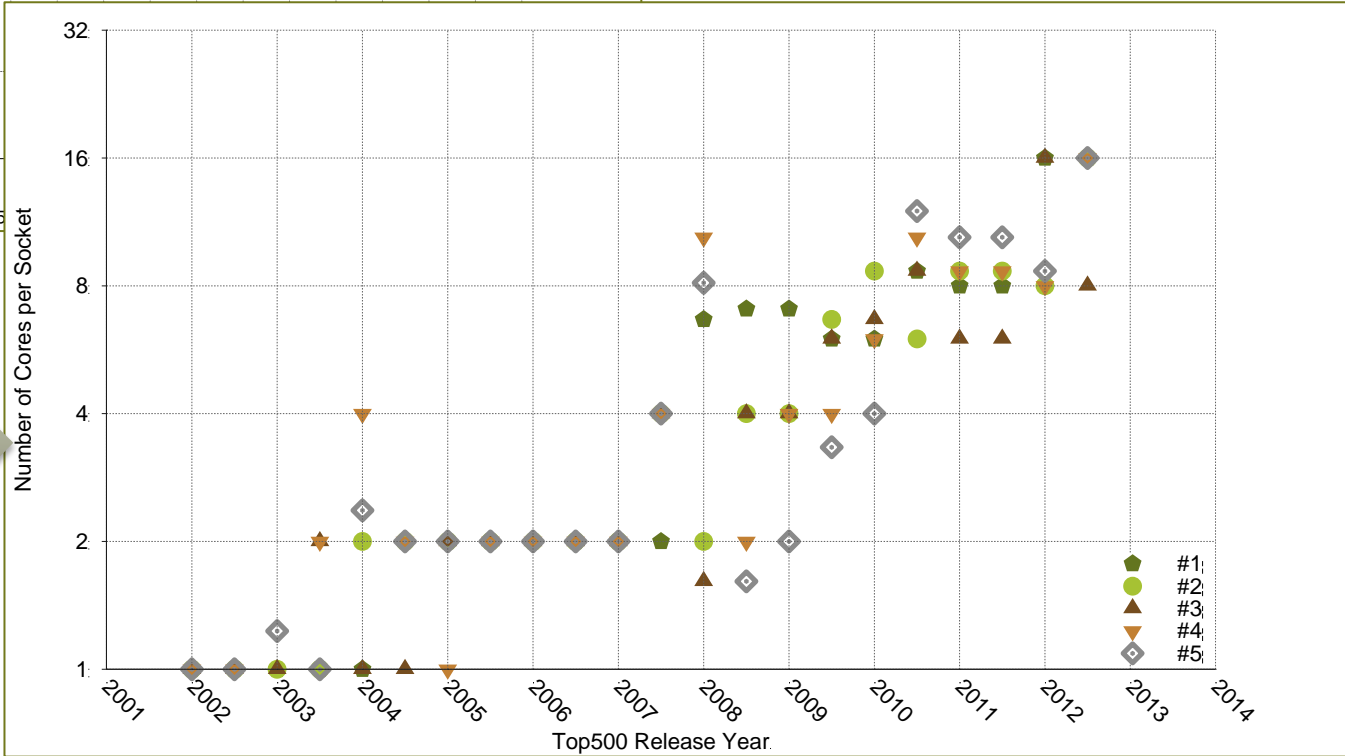
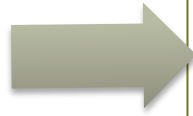


# Systems Growing, Decreasing in Reliability

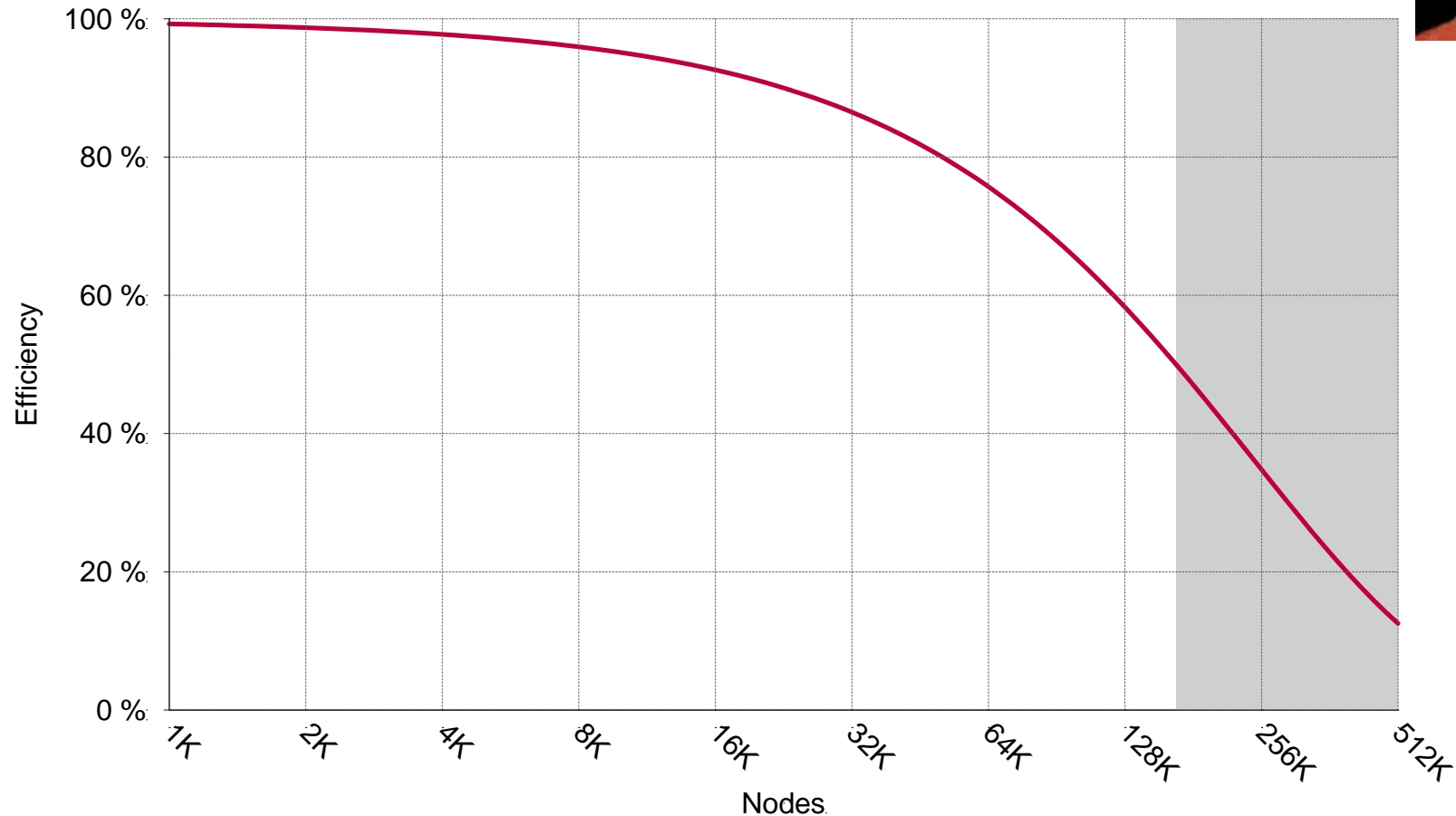


Systems are getting larger

Each node is getting more complicated



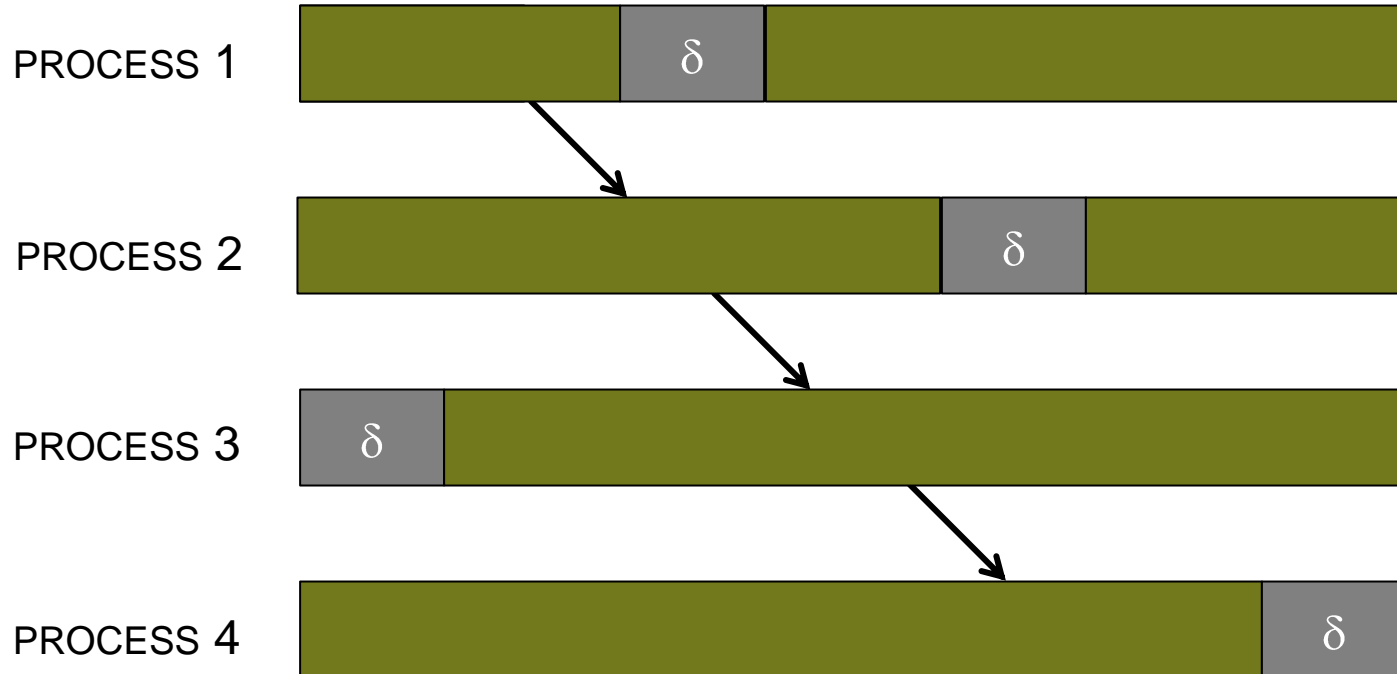
# Therefore Coordinated CR will not Scale



**Node MTBF: 25 years, Checkpoint/Restart Time: 5 minutes,  
Checkpoint Frequency: Optimal interval due to Young, walltime due to Daly**



# Uncoordinated Checkpointing to the Rescue



# uCR to the Rescue (cont'd)



- **Advantages:**

- Each node checkpoints independently, reducing expensive synchronization and possible resource contention
- Upon failure, only failed nodes restart rather than all nodes (may save power)

- **Drawbacks:**

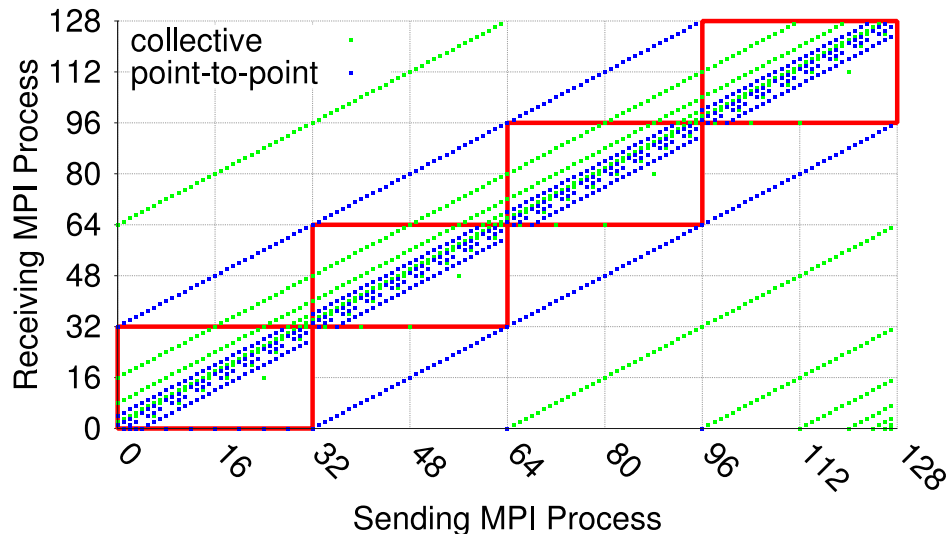
- Potentially expensive message logging protocols needed to ensure checkpoint consistency

# Related Work on Reducing Message Log Sizes

## ■ Send Determinism [IPDPS'11]

- Common deterministic property of applications that can be exploited to minimize message logging volume

## ■ Hierarchical (clustered) Checkpointing [IPDPS'12]

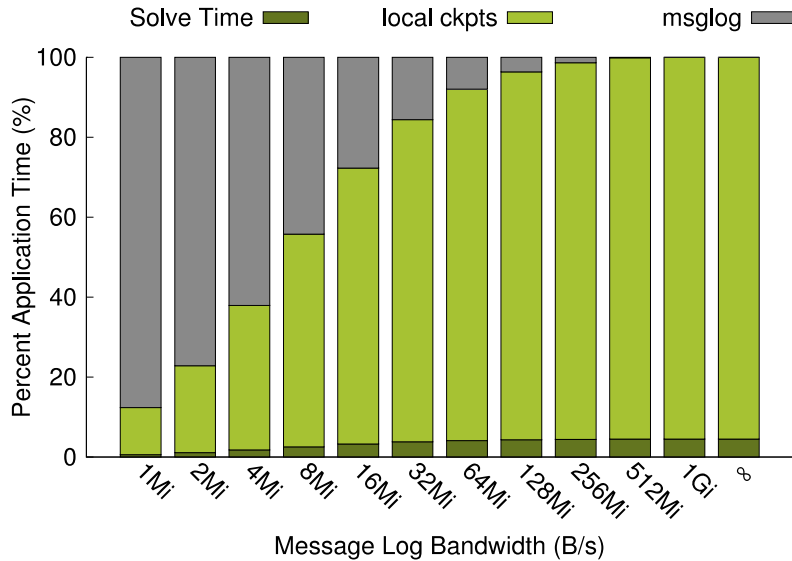


- cCR within a cluster
- uCR across clusters
- Only messages crossing clusters need to be logged

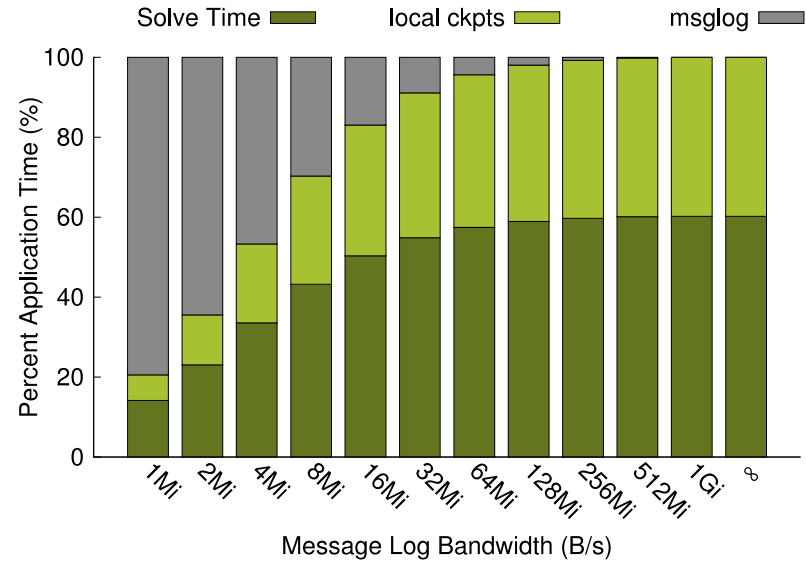
## ■ Demand checkpointing [HPDC'14]

- Reduce log size by forcing other processes to checkpoint

# As storage bandwidth increases, uCR checkpoint overheads dominate



CTH @ 64K Processes



LAMMPS @ 64K Processes

**Our Focus: uCR local checkpoint overheads**

# Questions to Consider

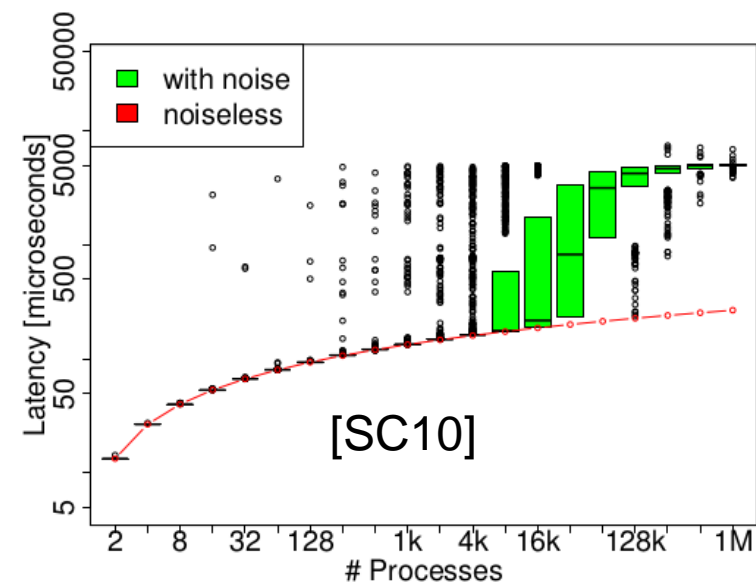


- **Question I: How does uCR perform at large scale?**
- **Question II: How does uCR compare with cCR at scale?**
- **Question III: What applications characteristics contribute to uCR's performance?**
- **Question IV: How can we improve uCR performance?**

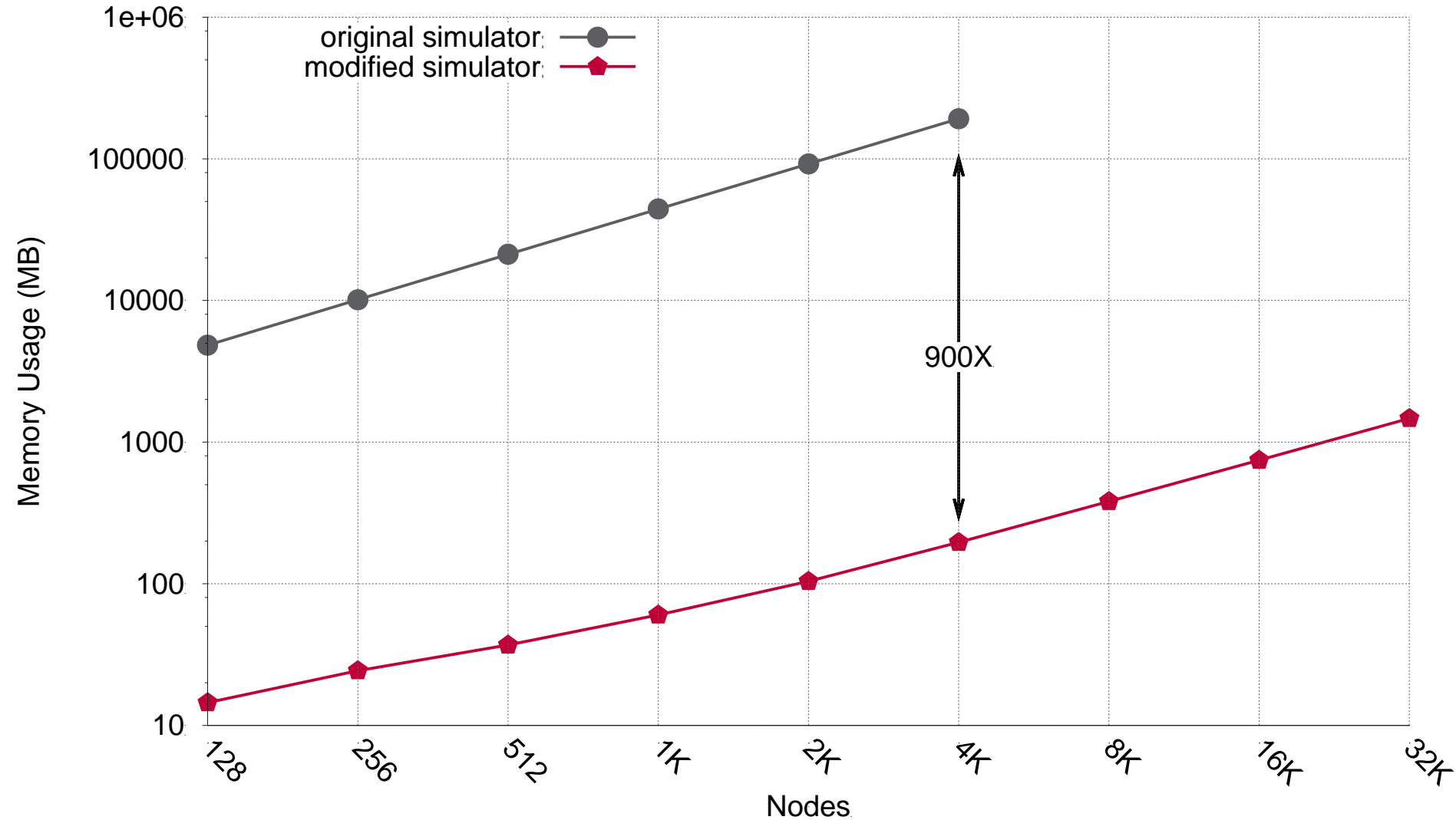


# Our Approach: Simulation

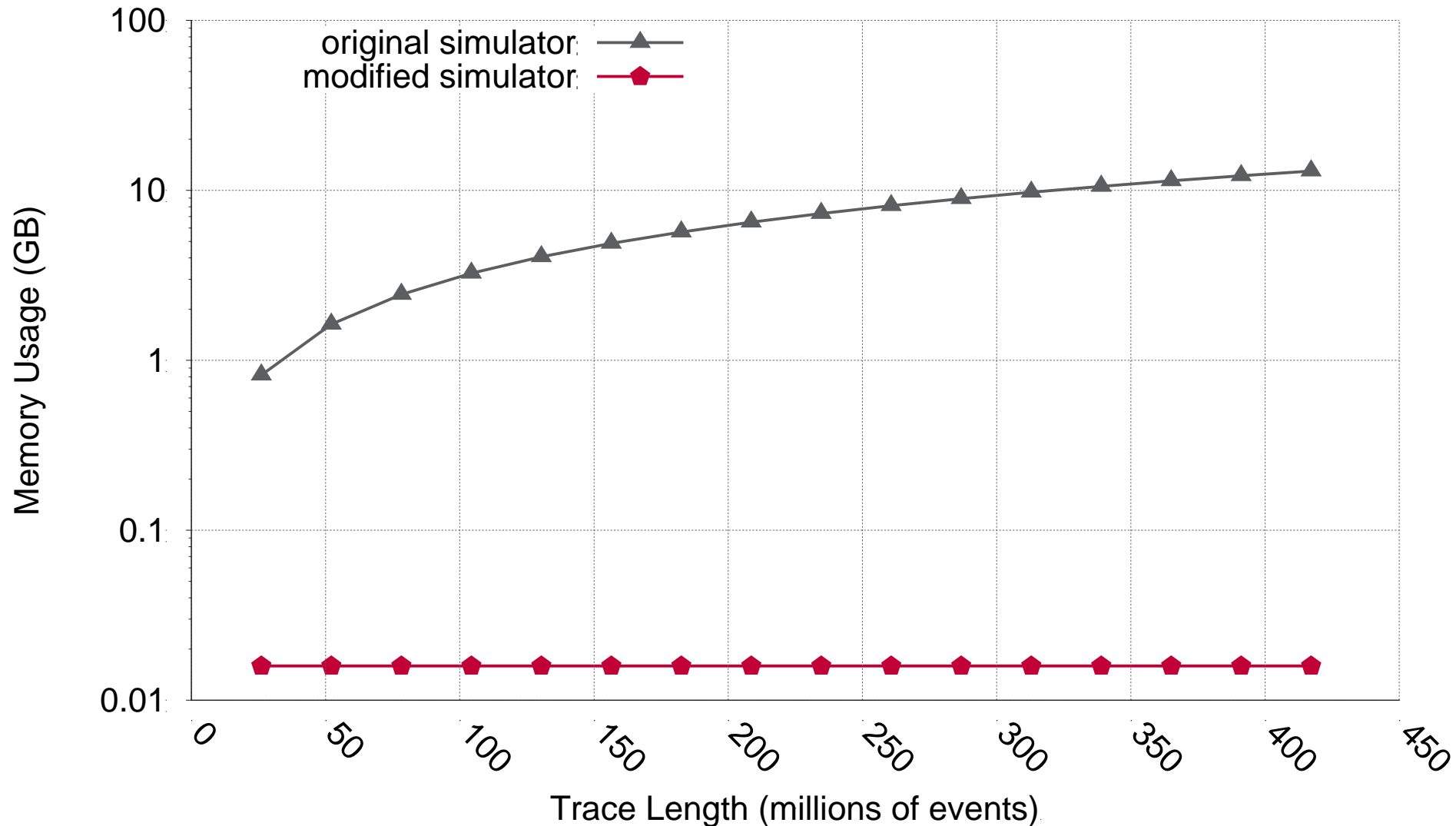
- **LogGOPSim-based simulation toolkit**
  - LogP-based simulator [LSAP'10]
  - Previously validated accurate for both cCR and uCR [PMBS'13]
  - Feed in application traces and overheads due to resilience mechanisms, get out per-node wall times
  - Increased scale achieved through trace extrapolation functionality
  
- **Extrapolation details:**
  - Collectives: Extrapolated accurately based on node count using well known algorithms
  - Point-to-point: Approximated using a weak-scaling application model



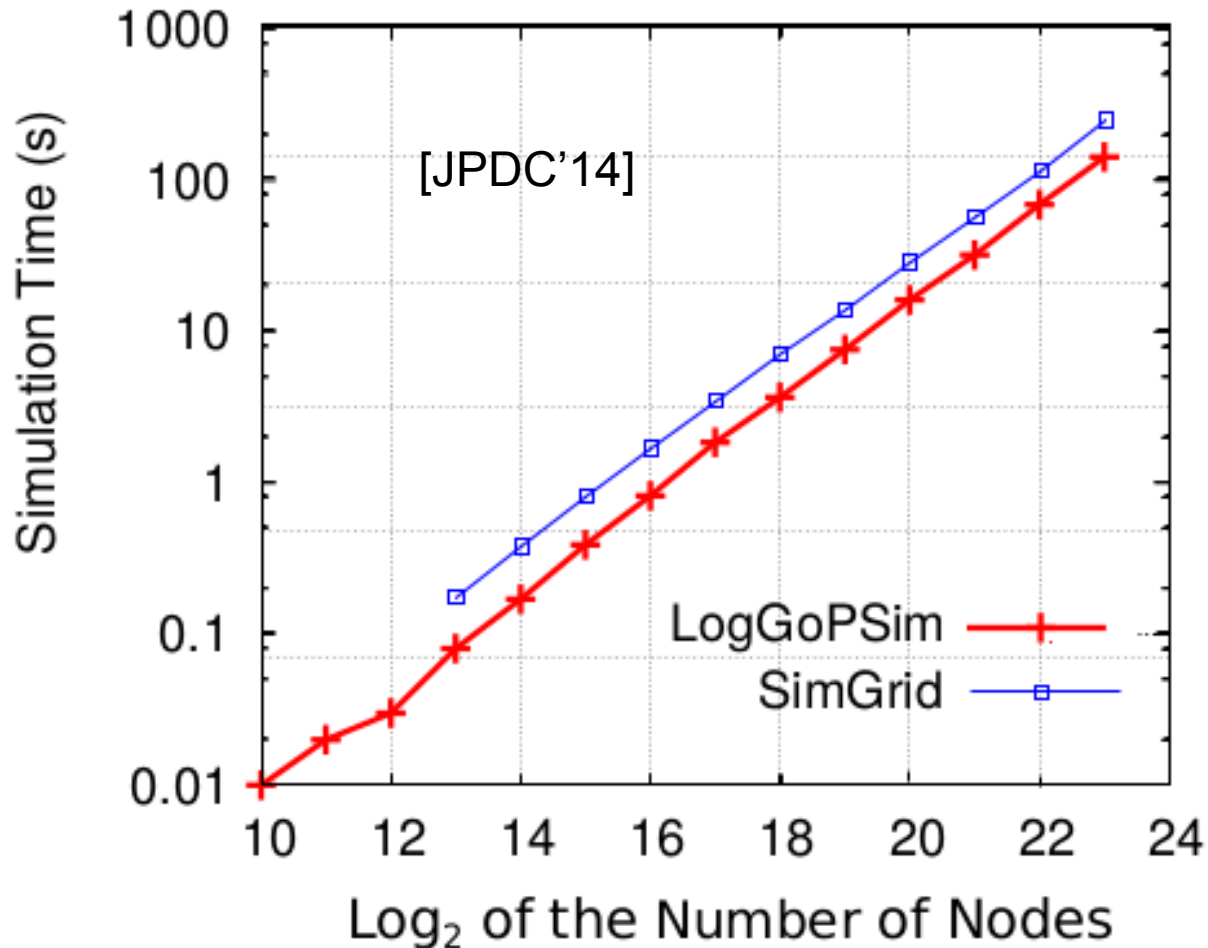
# LogGOPSim Extensions: In-Memory Extrapolation (size)



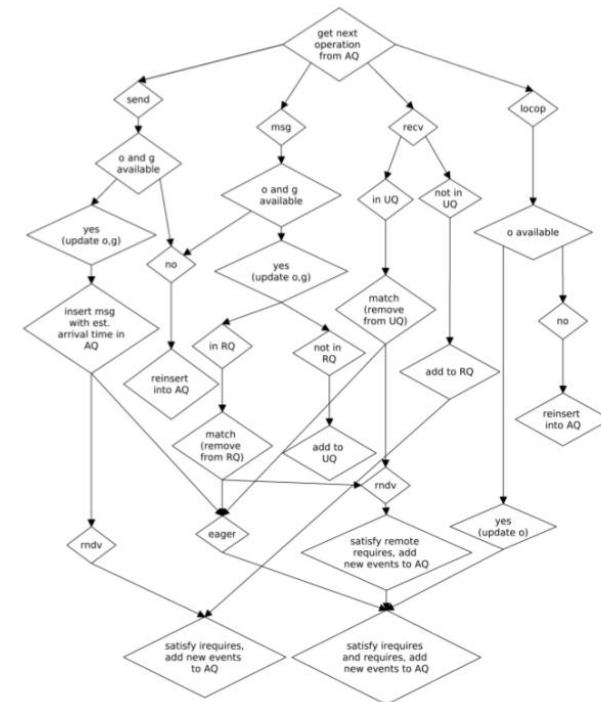
# LogGOPSim Extensions: In-Memory Extrapolation (time)



# LogGOPSim State of the Art Performance

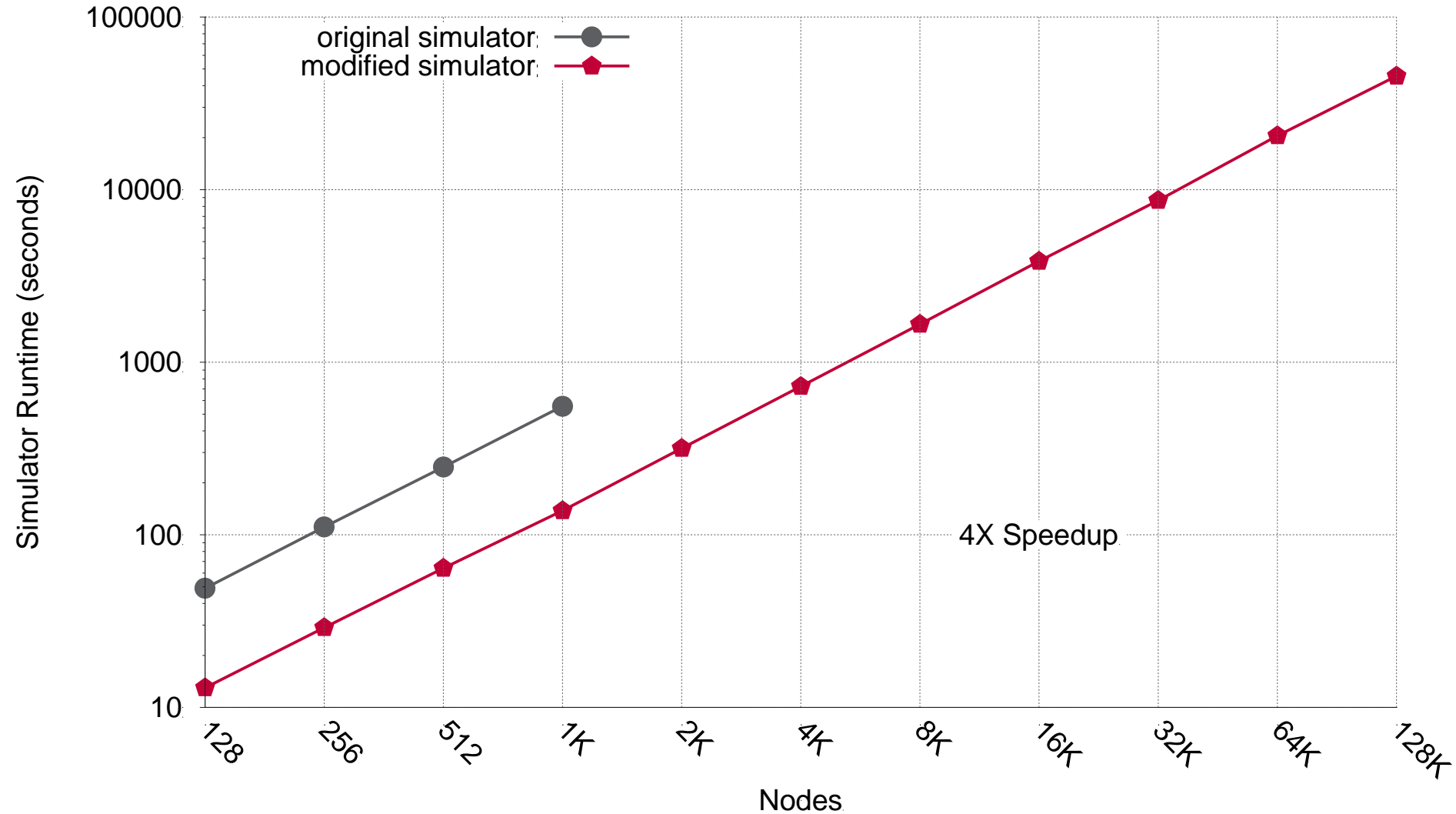


Source: [JPDC'14]



[LSAP'10]

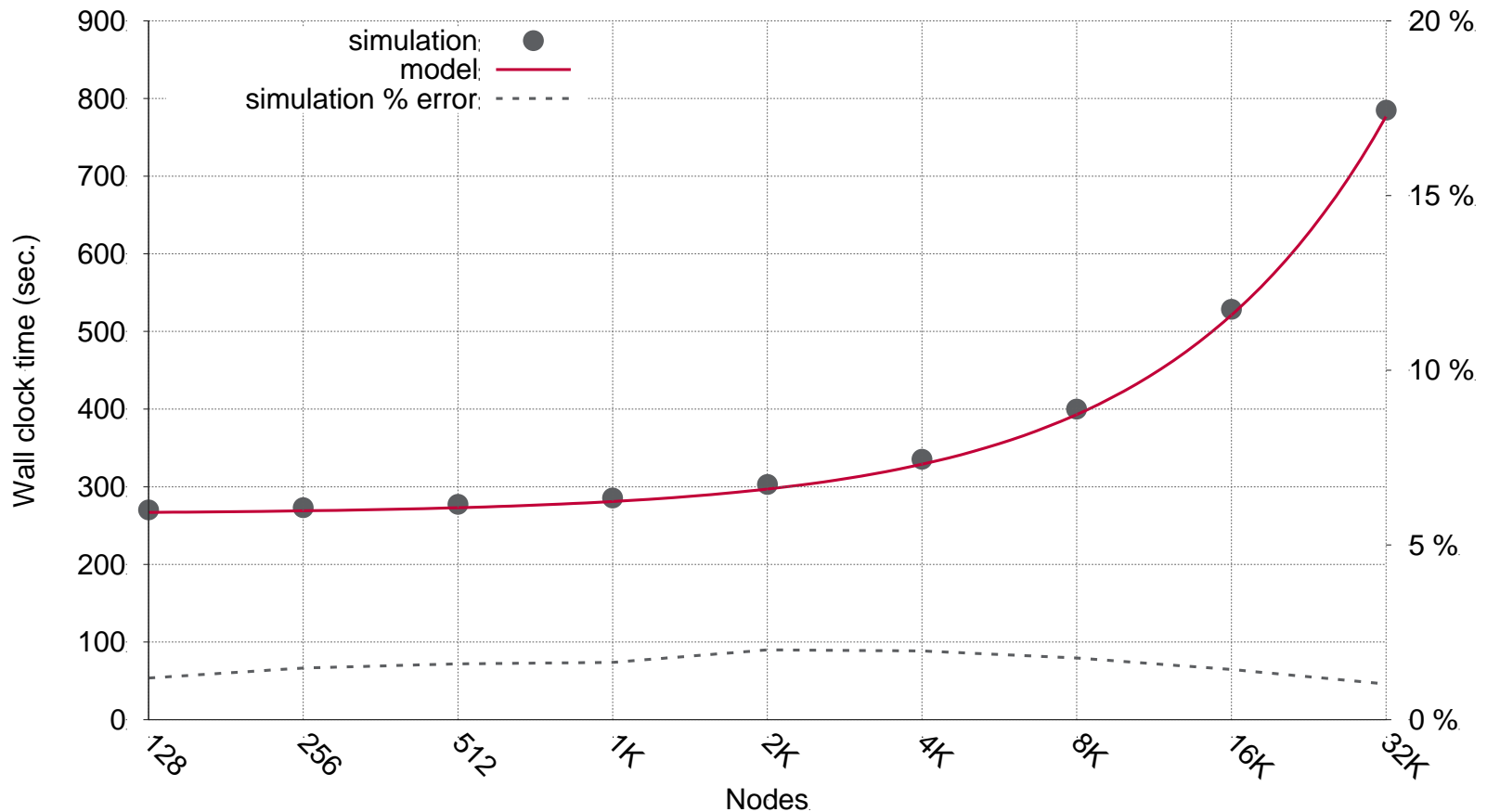
# LogGOPSim Extensions: Performance





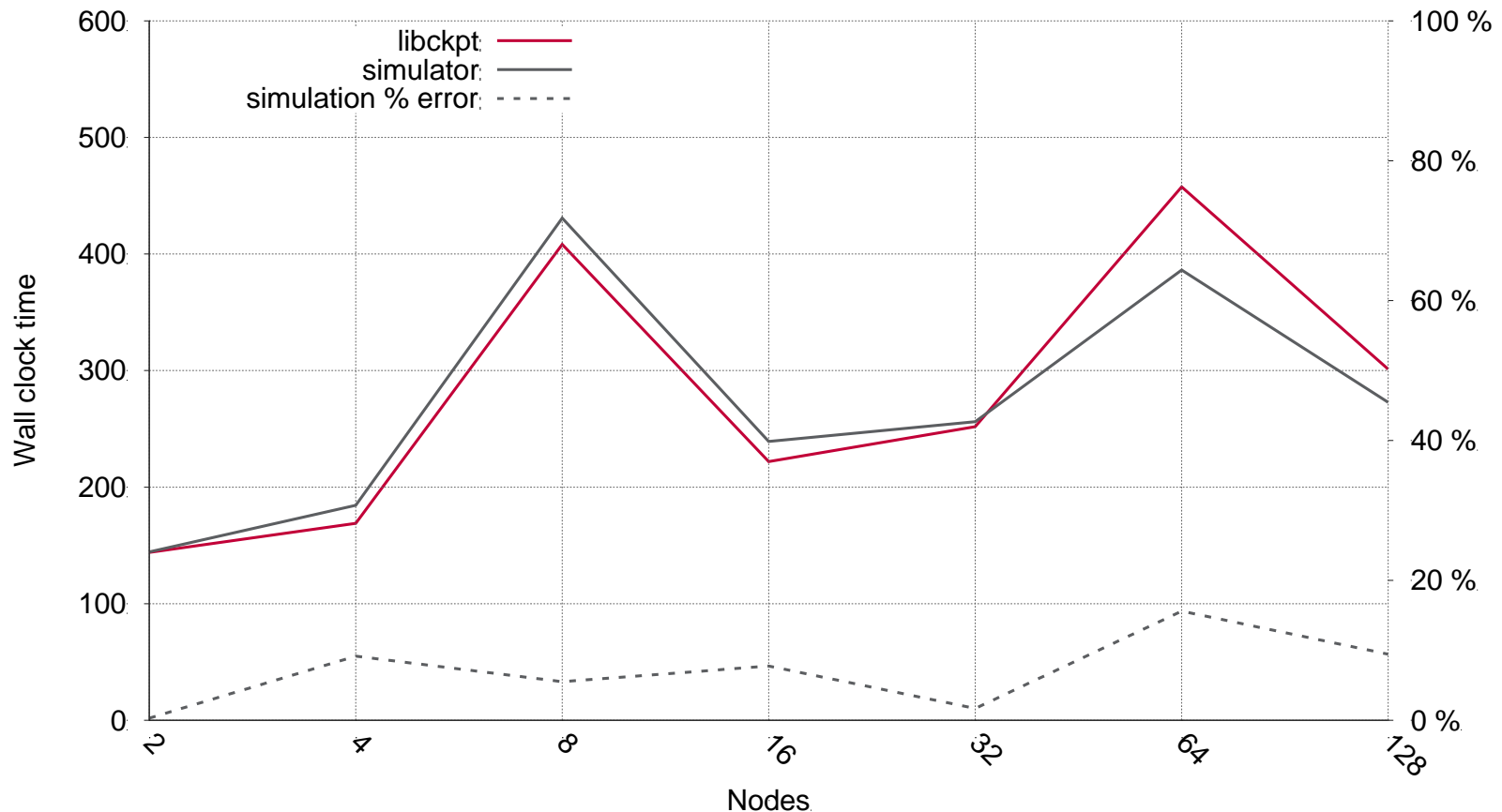
# Accuracy validation: analytic model

- **Model of failure-free coordinated checkpointing**
  - LAMMPS within 1%
  - CTH within 3% (see below)

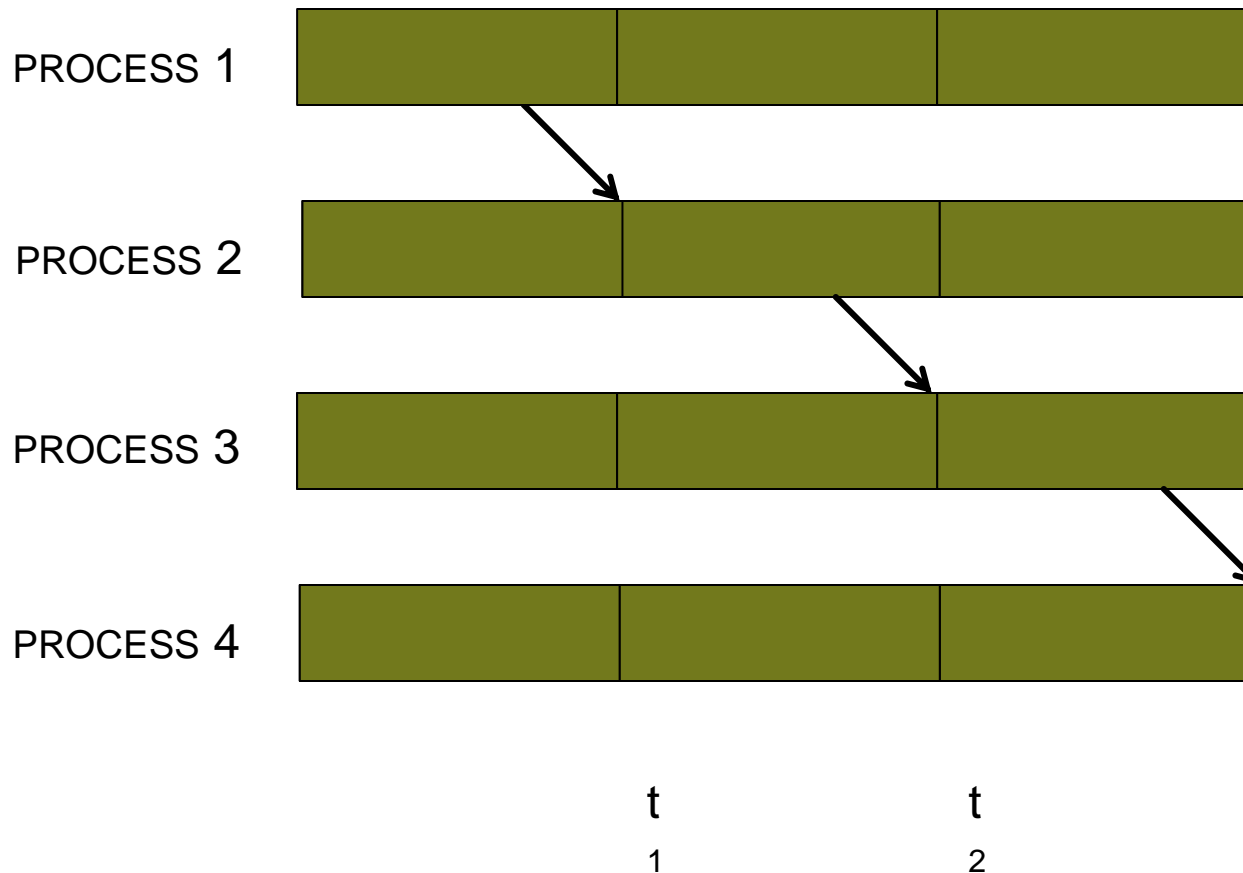


# Validation: small-scale testing

- Tests with coordinated & uncoordinated checkpointing
  - LAMMPS within 5%
  - CTH within 16% (coordinated checkpointing results shown)



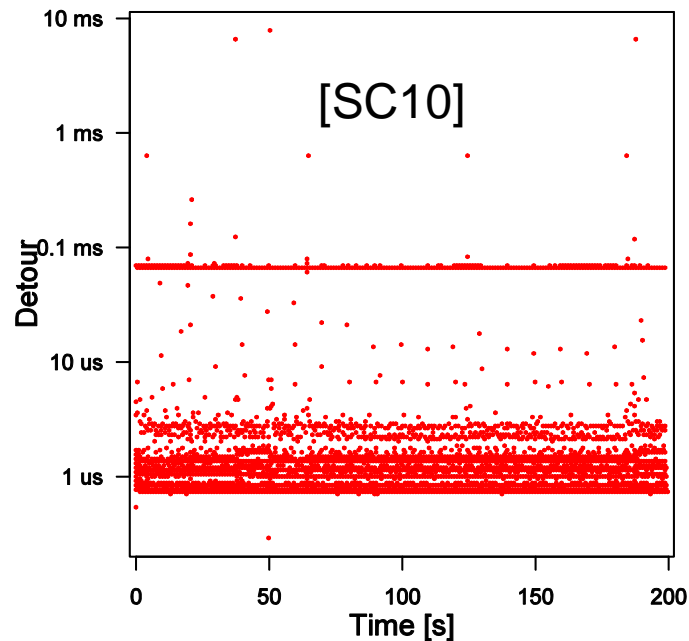
# Key Insight: Model uCR as Application Jitter



**Overheads due to Analogy with OS “Jitter” cf. [SC10]**

# Our Approach (cont'd)

- **Differences between OS noise [SC10] and resilience noise [PMBS'13]**
  - Resilience events order magnitude larger than typical OS interference events.
  - Noise playback is synchronous with application unlike asynchronous OS noise.



# Our Workloads and Setup

## ■ Key current and future workloads:

### ■ Current SNL Applications/Proxies:

*LAMMPS - molecular dynamics code from SNL*

*CTH - a shock physics code from SNL*

*HPCCG - conjugate gradient solver from mantevo suite*

### ■ Exascale Proxy Applications

*miniFE - a finite element benchmark from mantevo suite*

*LULESH – unstructured hydrodynamics benchmark*

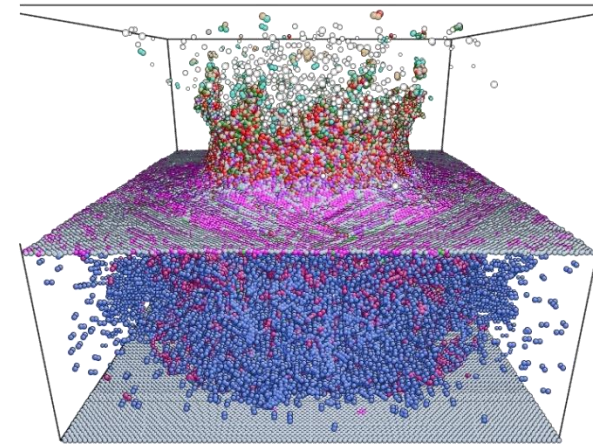
*MCCK - a neutronics proxy application*

## ■ Experimental parameters

■ uCR checkpoint duration: 1 second

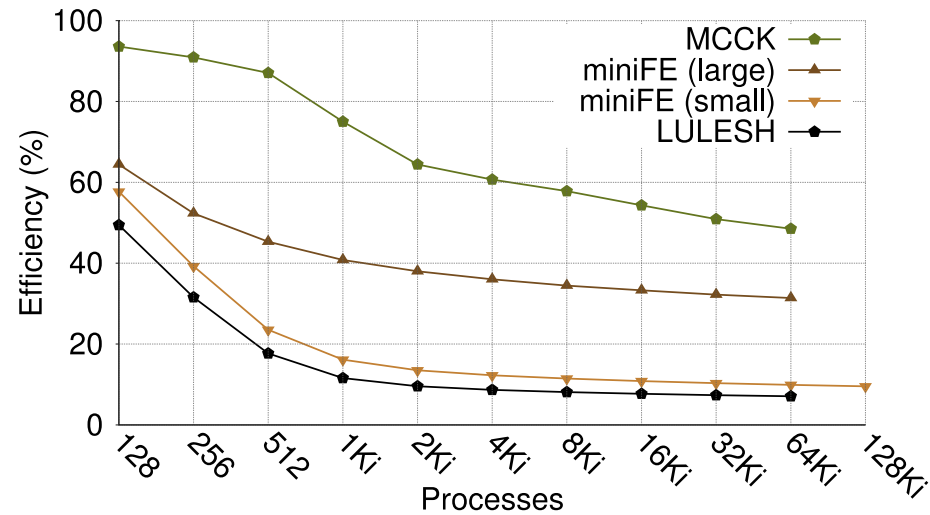
■ uCR checkpoint interval: 120 seconds

■ Each node checkpoints independently beginning with a random offset (worst-case scenario)

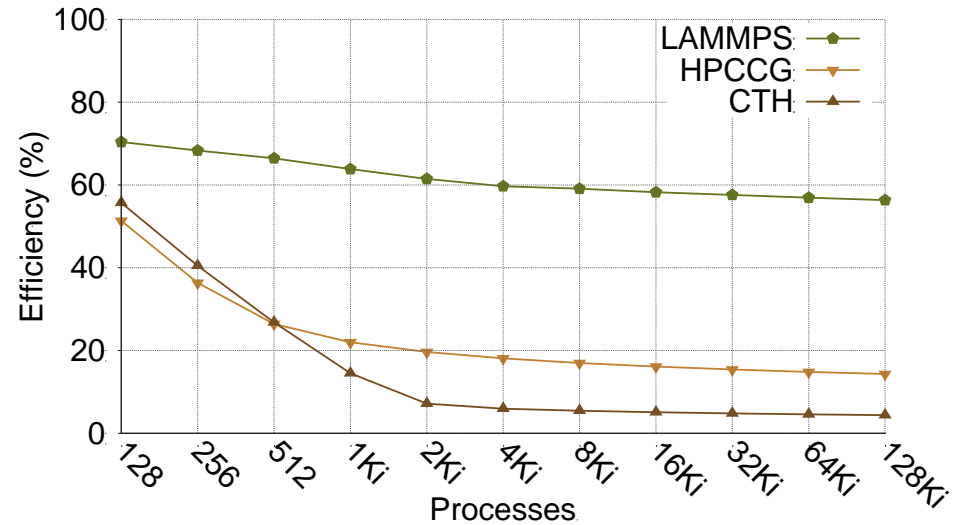




# Q.I: How does uCR perform at scale?



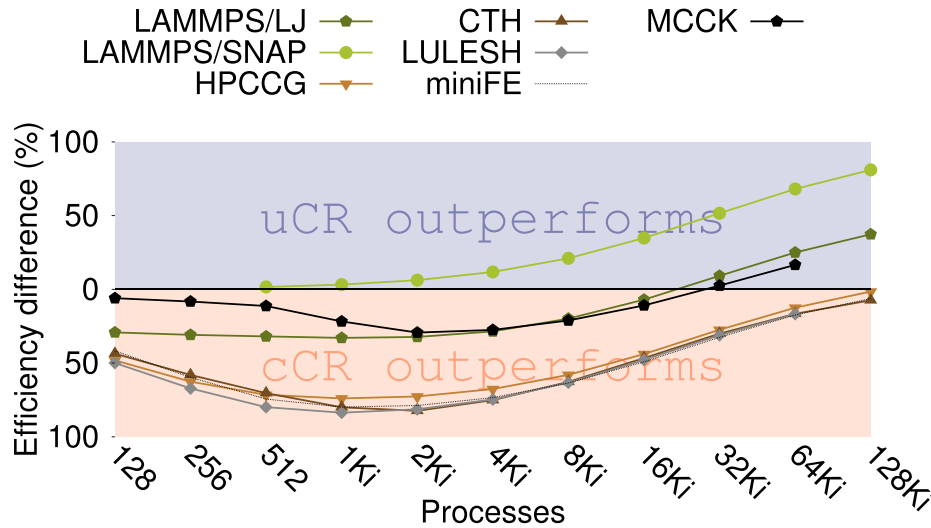
Co-Design Center Proxy Apps



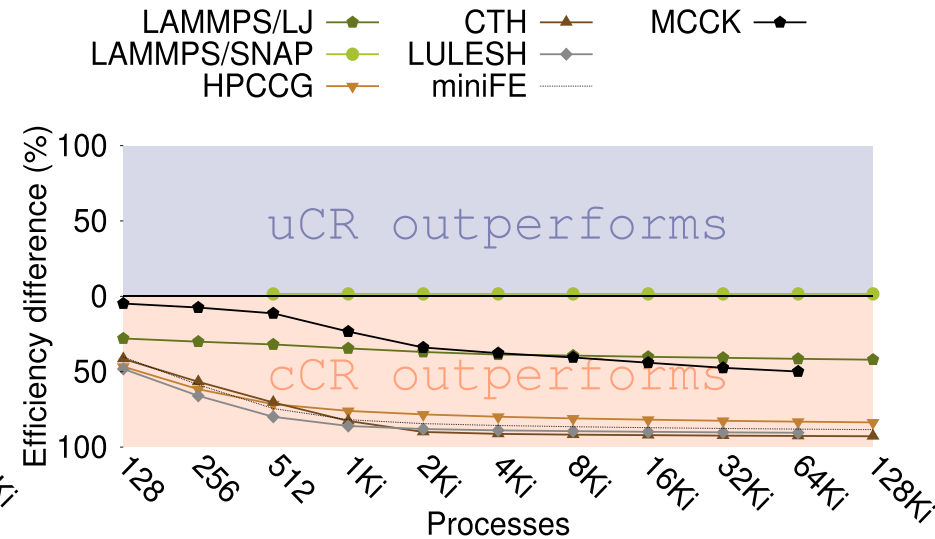
Sandia Workloads

**A1: Slowdowns can be significant and increase with scale**

## Q.II: How does uCR compare to cCR?



Parallel File System – 512 MiB/sec  
aggregate BW



Local Stable Storage (e.g., SSD)  
– 2 GiB/sec/process

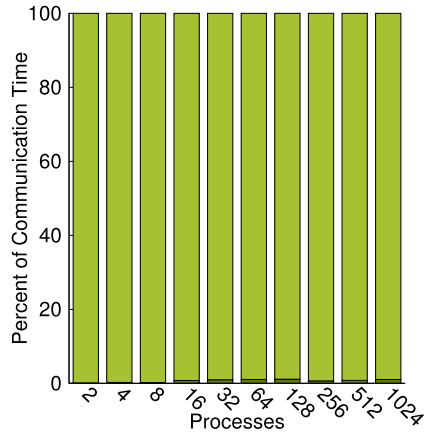
**A2: In bandwidth limited scenarios, uCR may outperform cCR**

# What is causing uCR slowdown?

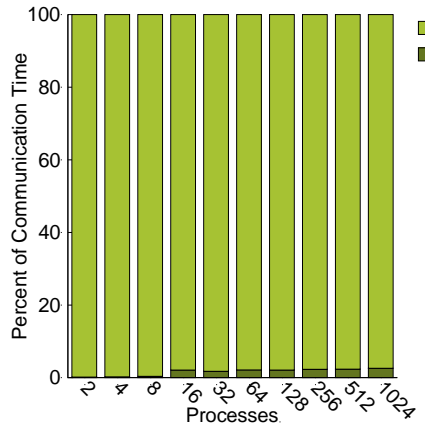
- Previous experience in OS noise helps guide this search:
  - a) Communication/Computation ratios?
  - b) Breakdown of communication operations (i.e., collectives)?
  - c) Algorithms used to implement collectives?
  - d) ???



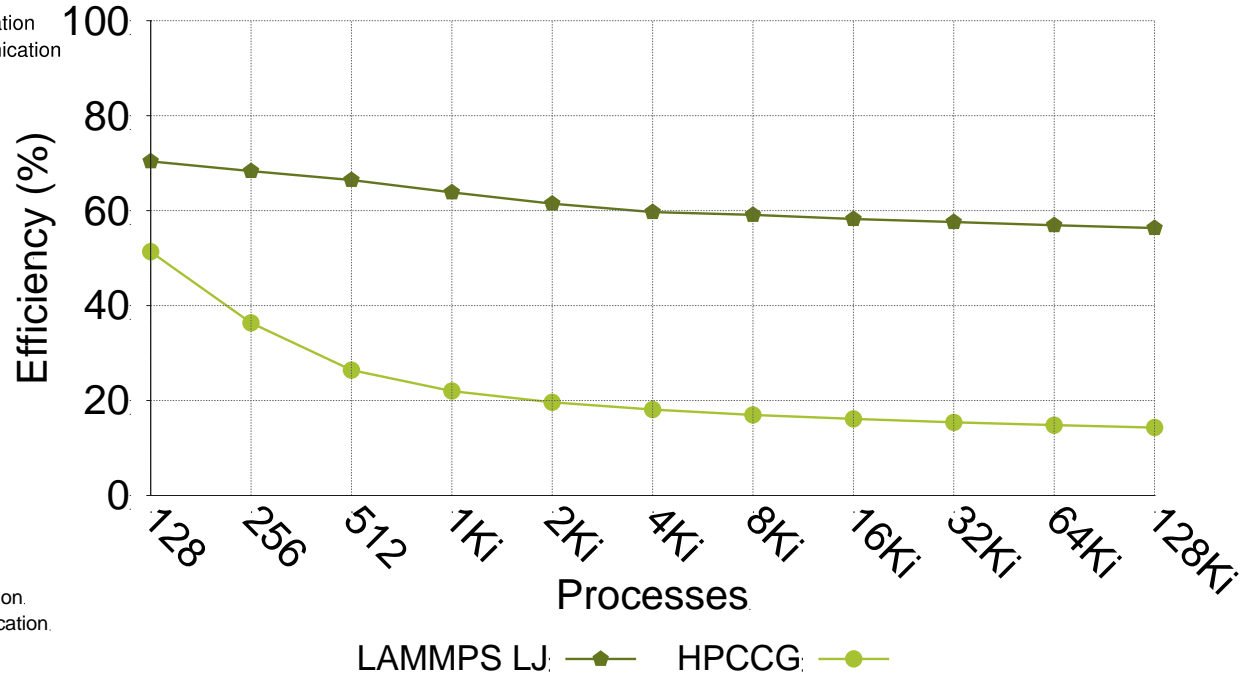
# a) Time Spent Communicating?



HPCCG

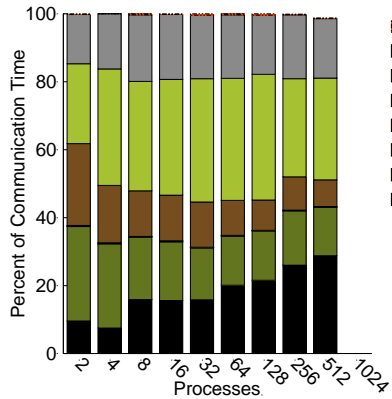


LAMMPS

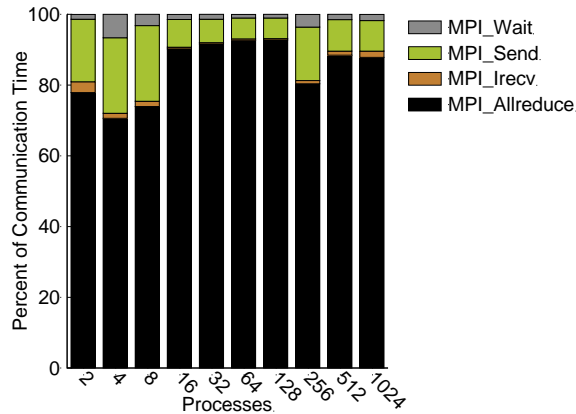


**Nope, does not appear to be correlated to time spent communicating**

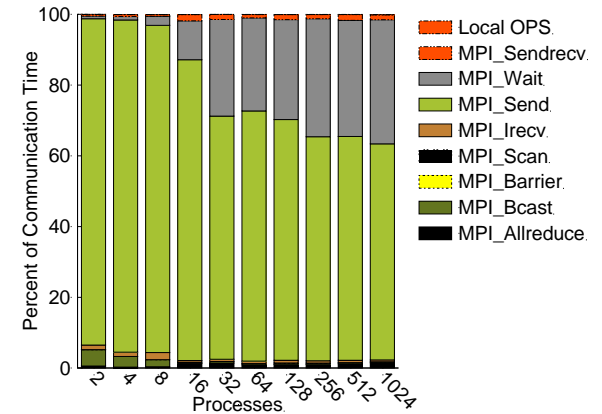
## b) Type of Communication?



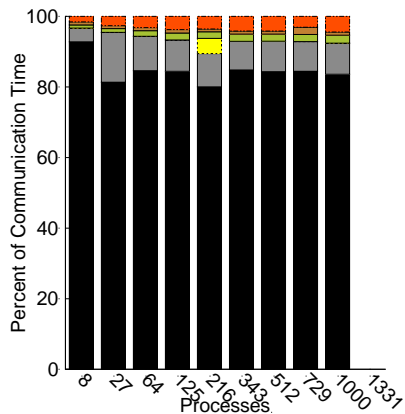
CTH



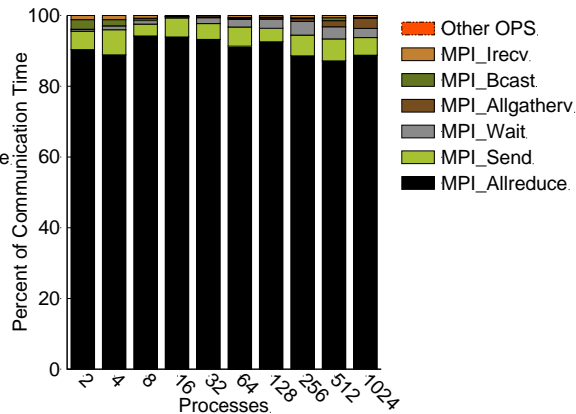
HPCCG



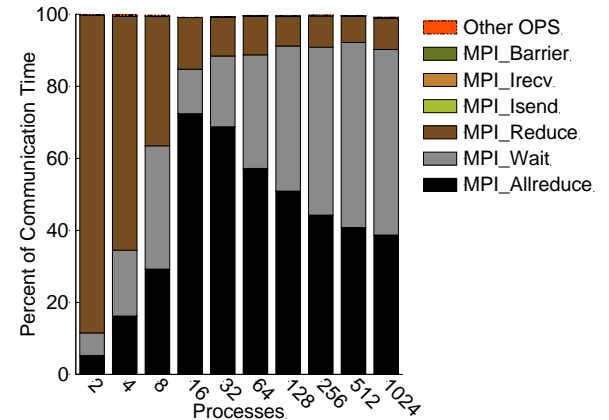
LAMMPS



LULESH



miniFE

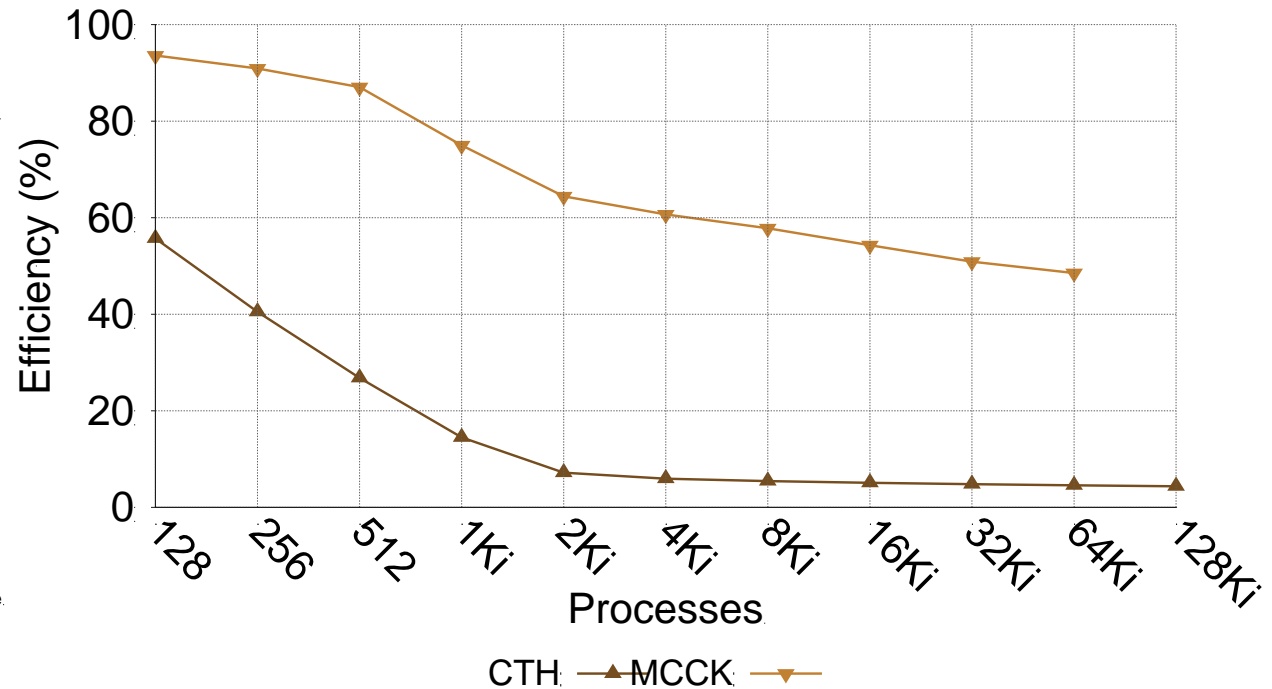
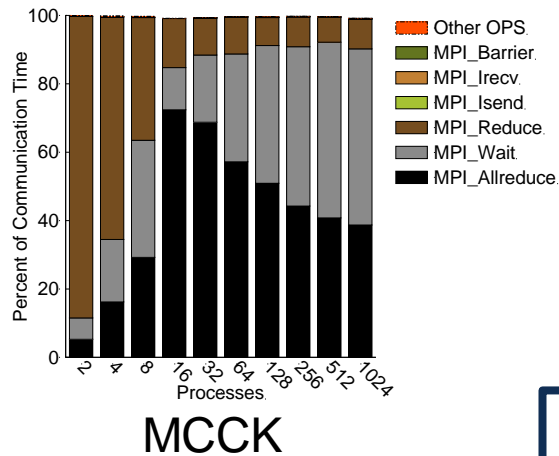
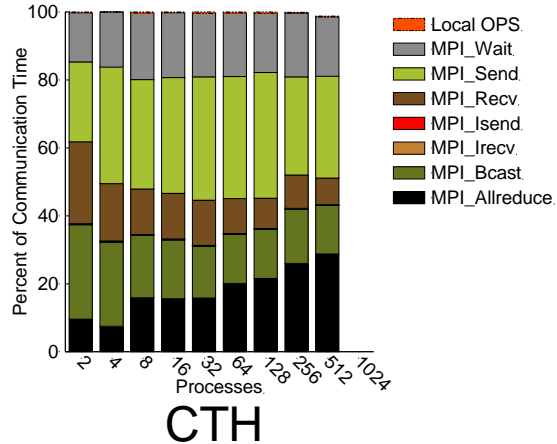


MCCK

Collective of choice is MPI\_Allreduce()



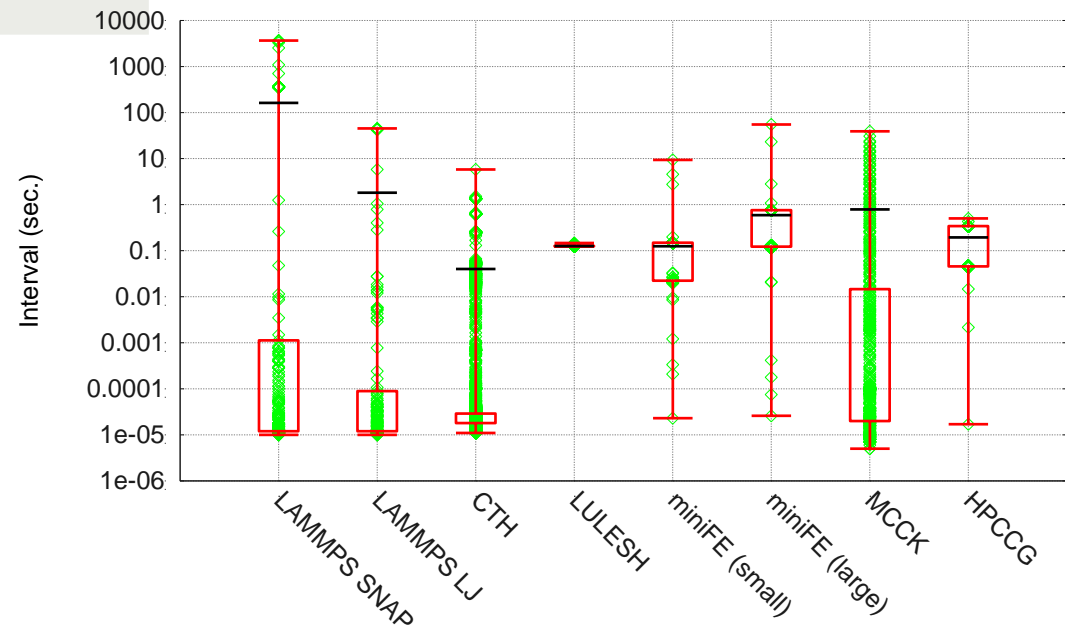
## c) Time in MPI\_Allreduce()?



**Nope, does not appear to be correlated to time in MPI\_Allreduce()**

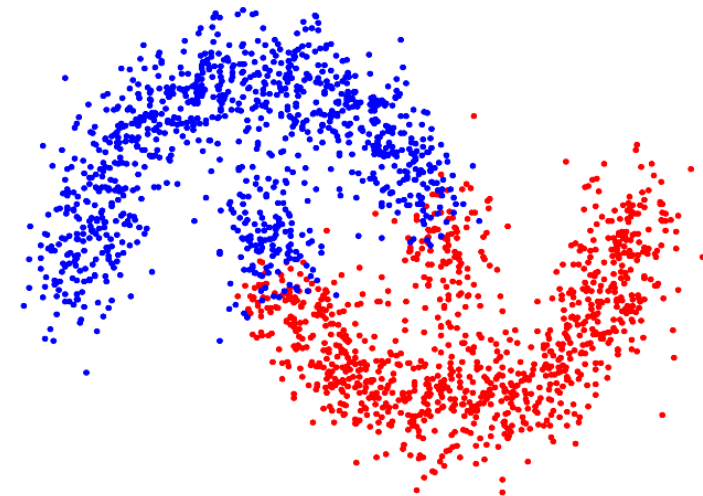
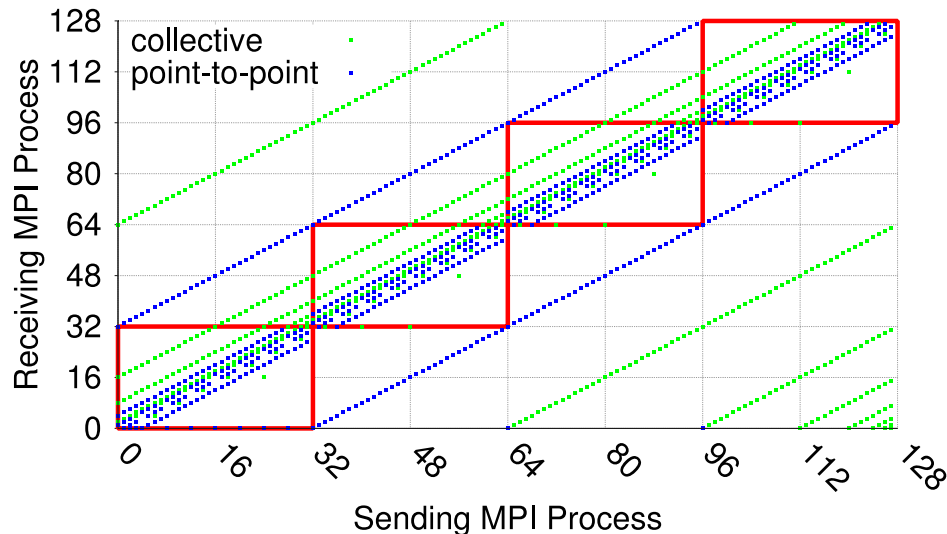
## d) Inter-arrival of MPI\_Allreduce() the Culprit!

App	Interarrival Avg	Efficiency
LAMMPS	1.8 seconds	70%
MCCK	0.79 seconds	50%
miniFE	0.59 seconds	30%
LULESH	0.13 seconds	10%
HPCCG	0.04 seconds	8%
CTH	0.03 seconds	5%



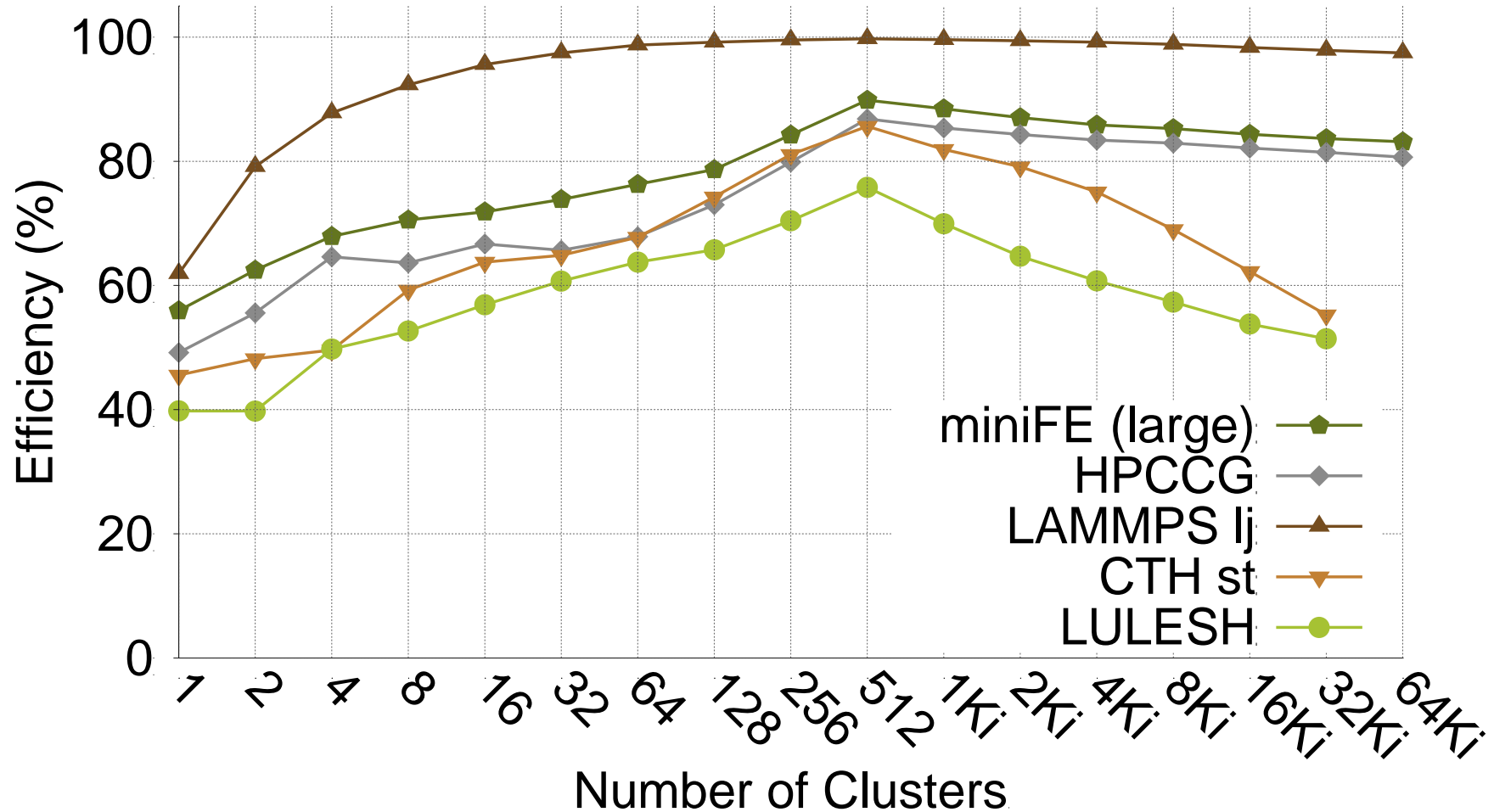
# Q.IV: How can we improve uCR performance?

- **cCR within a cluster**
  - Hierarchical (clustered) checkpointing approaches
- **uCR across clusters**
- **Only messages crossing clusters are logged**



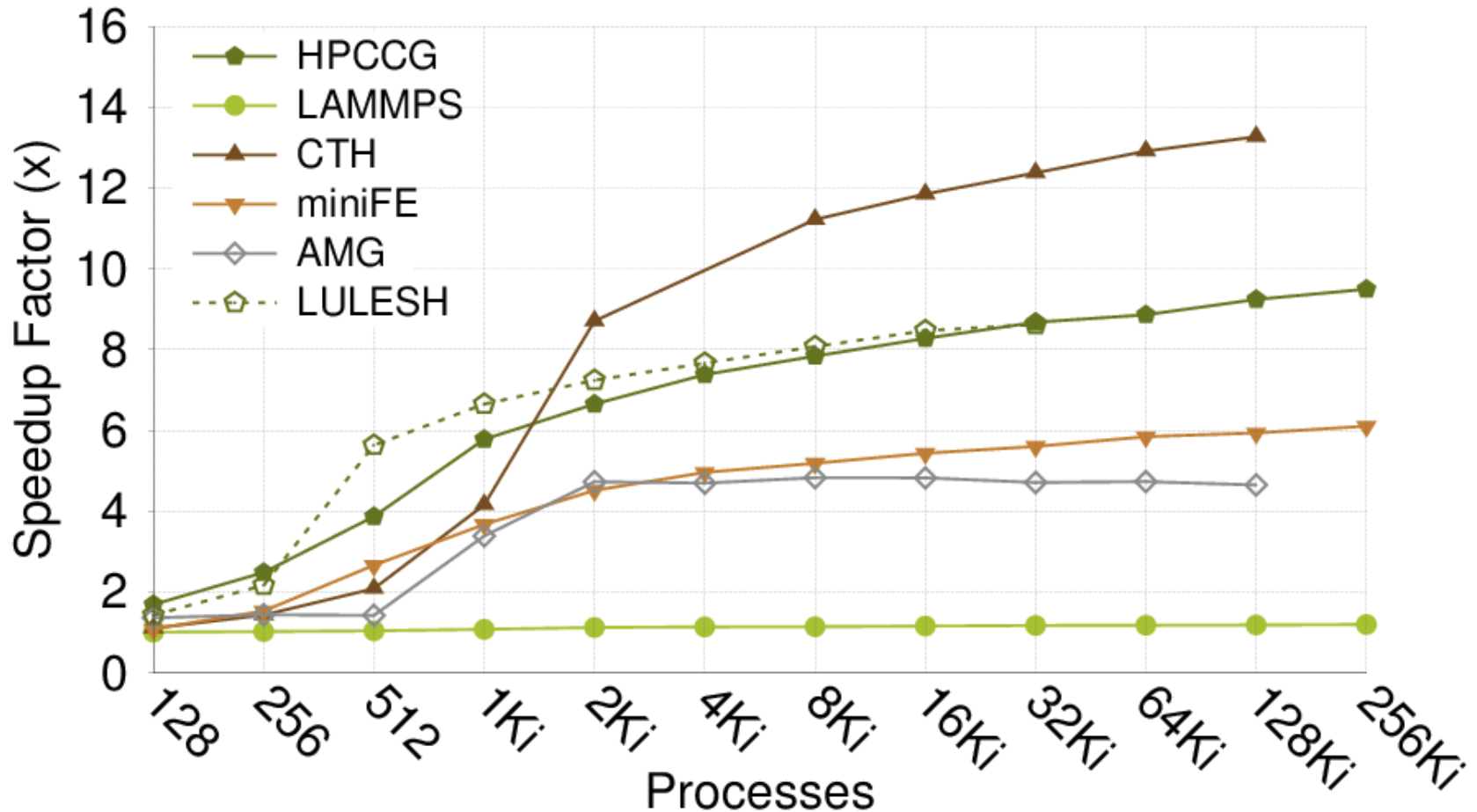
**In addition to reducing message log volumes can this technique improve performance?**

## A.IV: Clustering Improves uCR Performance



**Clustering improves performance because it reduces potential of overlapping noise events**

## A.IV: Nonblocking Collectives to the Rescue?



**Nonblocking collectives can improve the runtime substantially**

# What does this all mean?

## ■ What if ...

- I do not use collectives?

*Point-to-point operations can also create dependencies which may lead to significant (30%) slowdowns with uCR [SC14]*

- I use non-blocking collectives?

*If your code is capable of enough overlap, uCR may work well [EuroMPI'14]*

- I have an over-decomposed, many-task model?

*Similar to non-blocking collectives, uCR may work well but your runtime may need to consider the dependencies created in communication.*



# Key Take Home Messages



- At current and future stable storage bandwidths, the cost of local checkpoints for uCR can have a greater impact than message logging overheads
- This cost is dictated by happens-before chains created by an application's communication pattern
- uCR protocols based on process clustering can be used to tune an application's performance sensitivity to local checkpointing activities
- In uCR protocols, collective communication limits the progress which surviving processes make once a failure has occurred

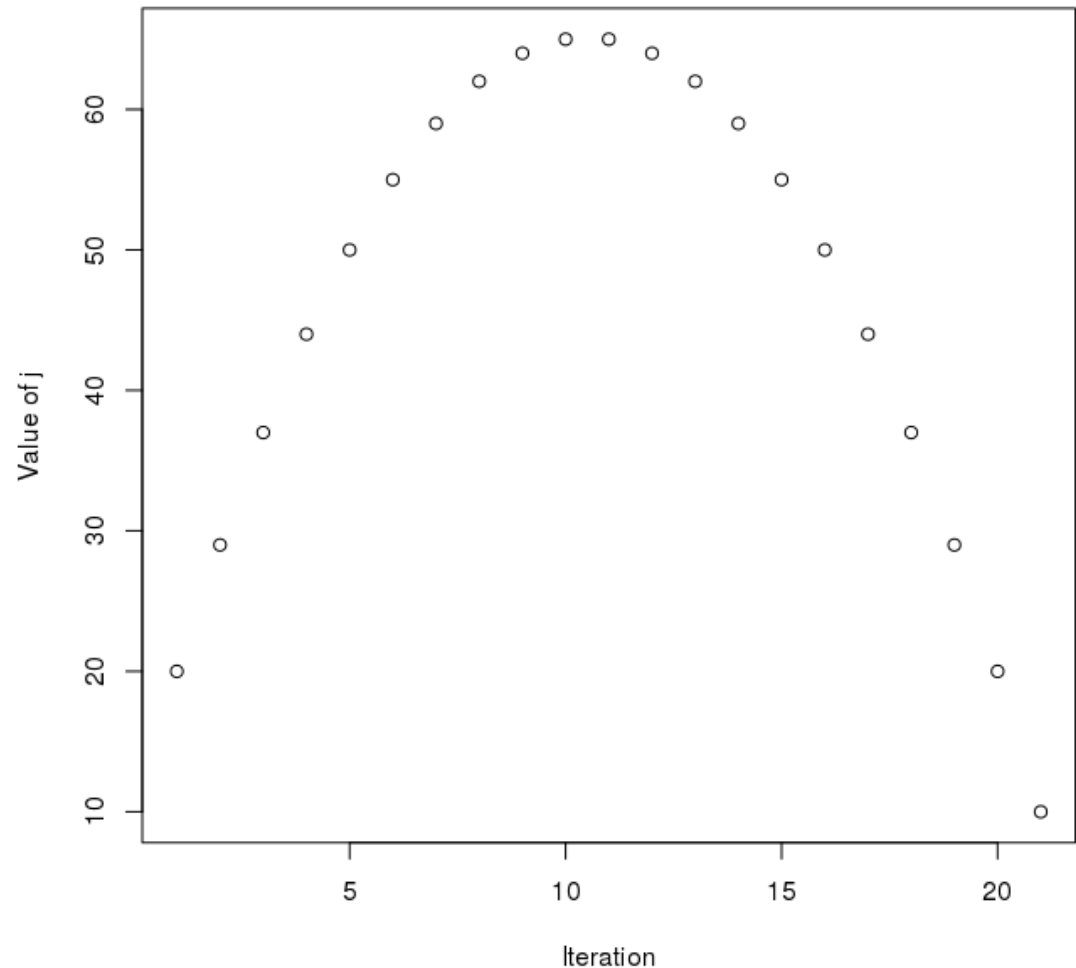


# Application Scalability: Counting Loop Iterations

- When the polyhedral model cannot handle it

```
j=10;  
k=10;  
while (j>0) {  
    j=j+k;  
    k--;  
}
```

?



# Counting Arbitrary Affine Loop Nests

## ■ Affine loops

---

```

x=x0;           // Initial assignment
while(cTx < g)  // Loop guard
  x=Ax + b;      // Loop update
  
```

---

## ■ Perfectly nested affine loops

---

```

while(c1Tx < g1) {
  x = A1x + b1;
  while(c2Tx < g2) {
    ...
    x = Ak-1x + bk-1;
    while(ckTx < gk) {
      x = Akx + bk;
      while(ck+1Tx < gk+1) {... }
      x = Ukx + vk; }
    x = Uk-1x + vk-1;
    ... }
  x = U1x + v1;}
  
```

---

$A_k, U_k \in \mathbb{R}^{m \times m}$ ,  $b_k, v_k, c_k \in \mathbb{R}^m$ ,  $g_k \in \mathbb{R}$  and  $k = 1 \dots r$ .

# Counting Arbitrary Affine Loop Nests

- Example

```
for (j=1; j < n/p + 1; j= j*2)
    for (k=j; k < m; k = k + j )
        veryComplicatedOperation(j,k);
```

# Counting Arbitrary Affine Loop Nests

## ■ Example

```

for (j=1; j < n/p + 1; j= j*2)
  for (k=j; k < m; k = k + j )
    veryComplicatedOperation(j,k);

```

---

```

while ( $c_1^T x < g_1$ ) {
   $x = A_1 x + b_1$ ;
  while ( $c_2^T x < g_2$ ) {
    ...
     $x = A_{k-1} x + b_{k-1}$ ;
    while ( $c_k^T x < g_k$ ) {
       $x = A_k x + b_k$ ;
      while ( $c_{k+1}^T x < g_{k+1}$ ) { ... }
       $x = U_k x + v_k$ ; }
     $x = U_{k-1} x + v_{k-1}$ ;
    ... }
   $x = U_1 x + v_1$ ; }

```

---

# Counting Arbitrary Affine Loop Nests

## ■ Example

```

for (j=1; j < n/p + 1; j= j*2)
  for (k=j; k < m; k = k + j )
    veryComplicatedOperation(j,k);

```

$$\begin{pmatrix} j \\ k \end{pmatrix} = \begin{pmatrix} 0 & 0 \\ 0 & 1 \end{pmatrix} \begin{pmatrix} j \\ k \end{pmatrix} + \begin{pmatrix} 1 \\ 0 \end{pmatrix};$$

---

```

while (c1Tx < g1) {
  x = A1x + b1;
  while (c2Tx < g2) {
    ...
    x = Ak-1x + bk-1;
    while (ckTx < gk) {
      x = Akx + bk;
      while (ck+1Tx < gk+1) { ... }
      x = Ukx + vk; }
    x = Uk-1x + vk-1;
    ... }
  x = U1x + v1; }

```

---

# Counting Arbitrary Affine Loop Nests

## ■ Example

```

for (j=1; j < n/p + 1; j= j*2)
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$$\text{while}(\mathbf{1} \ 0 \begin{pmatrix} j \\ k \end{pmatrix} < n/p + 1) \{$$

---

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```

---

}

# Counting Arbitrary Affine Loop Nests

## ■ Example

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```

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$$\text{while}((1 \ 0) \begin{pmatrix} j \\ k \end{pmatrix} < n/p + 1) \{$$

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$$\text{while}((0 \ 1) \begin{pmatrix} j \\ k \end{pmatrix} < m) \{$$

$$\}$$

$$\}$$


---

```

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  x = A1x + b1;
  while (c2Tx < g2) {
    ...
    x = Ak-1x + bk-1;
    while (ckTx < gk) {
      x = Akx + bk;
      while (ck+1Tx < gk+1) { ... }
      x = Ukx + vk; }
    x = Uk-1x + vk-1;
    ... }
  x = U1x + v1; }

```

---



# Counting Arbitrary Affine Loop Nests

## ■ Example

```

for (j=1; j < n/p + 1; j= j*2)
  for (k=j; k < m; k = k + j )
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$$\text{while}((1 \ 0) \begin{pmatrix} j \\ k \end{pmatrix} < n/p + 1) \{$$

$$\begin{pmatrix} j \\ k \end{pmatrix} = \begin{pmatrix} 1 & 0 \\ 1 & 0 \end{pmatrix} \begin{pmatrix} j \\ k \end{pmatrix} + \begin{pmatrix} 0 \\ 0 \end{pmatrix};$$

$$\text{while}((0 \ 1) \begin{pmatrix} j \\ k \end{pmatrix} < m) \{$$

$$\begin{pmatrix} j \\ k \end{pmatrix} = \begin{pmatrix} 1 & 0 \\ \mathbf{1} & \mathbf{1} \end{pmatrix} \begin{pmatrix} j \\ k \end{pmatrix} + \begin{pmatrix} 0 \\ 0 \end{pmatrix};$$

$$\} \begin{pmatrix} j \\ k \end{pmatrix} = \begin{pmatrix} \mathbf{2} & 0 \\ 0 & 1 \end{pmatrix} \begin{pmatrix} j \\ k \end{pmatrix} + \begin{pmatrix} 0 \\ 0 \end{pmatrix};$$

$$\}$$


---

```

while (c1Tx < g1) {
  x = A1x + b1;
  while (c2Tx < g2) {
    ...
    x = Ak-1x + bk-1;
    while (ckTx < gk) {
      x = Akx + bk;
      while (ck+1Tx < gk+1) { ... }
      x = Ukx + vk; }
    x = Uk-1x + vk-1;
    ... }
  x = U1x + v1; }

```

---

# Counting Arbitrary Affine Loop Nests

## ■ Example

```

for (j=1; j < n/p + 1; j= j*2)
  for (k=j; k < m; k = k + j )
    veryComplicatedOperation(j,k);
  
```

$$x = \begin{pmatrix} 0 & 0 \\ 0 & 1 \end{pmatrix} x + \begin{pmatrix} 1 \\ 0 \end{pmatrix};$$

$while((1 \ 0)x < \frac{n}{p} + 1)\{$

$$x = \begin{pmatrix} 1 & 0 \\ 1 & 0 \end{pmatrix} x + \begin{pmatrix} 0 \\ 0 \end{pmatrix};$$

$while((0 \ 1)x < m)\{$

$$x = \begin{pmatrix} 1 & 0 \\ 1 & 1 \end{pmatrix} x + \begin{pmatrix} 0 \\ 0 \end{pmatrix}$$

$$\} x = \begin{pmatrix} 2 & 0 \\ 0 & 1 \end{pmatrix} x + \begin{pmatrix} 0 \\ 0 \end{pmatrix};$$

$\}$

where  $x = \begin{pmatrix} j \\ k \end{pmatrix}$

---

```

while( $c_1^T x < g_1$ ) {
   $x = A_1 x + b_1$ ;
  while( $c_2^T x < g_2$ ) {
    ...
     $x = A_{k-1} x + b_{k-1}$ ;
    while( $c_k^T x < g_k$ ) {
       $x = A_k x + b_k$ ;
      while( $c_{k+1}^T x < g_{k+1}$ ) { ... }
       $x = U_k x + v_k$ ; }
     $x = U_{k-1} x + v_{k-1}$ ;
    ... }
   $x = U_1 x + v_1$ ; }
  
```

---

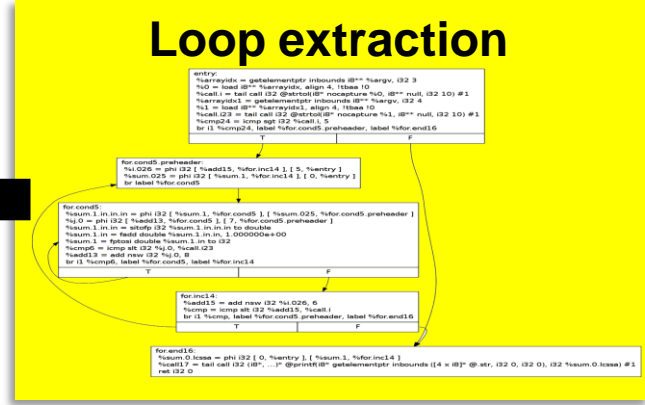
# Current Workflow

### Parallel program

```

do i = 1, procCols
  call mpi_irecv( buff, 1, dp_type, reduce_exch_proc(i),
    > i, mpi_comm_world, request, ierr )
  call mpi_send( buff2, 1, dp_type, reduce_exch_proc(i),
    > i, mpi_comm_world, ierr )
  call mpi_wait( request, status, ierr )
enddo

do i = id * n/p, ( id + 1 ) * n/p
  do j = 1, nSize
    call compute
  
```



### Closed form representation

$$x(i_1, \dots, i_r) = A_{final}(i_1, \dots, i_r) \cdot x_0 + b_{final}(i_1, \dots, i_r)$$

with

$$i_r = 0 \dots n_k(x_{0,k}), k = 1 \dots r$$

### Affine loop synthesis

```

while (c1^T x < g1) {
  x = A1x + b1;
  while (c2^T x < g2) {
    ...
    x = Ak-1x + bk-1;
    while (ck^T x < gk) {
      x = Akx + bk;
      while (c_{k+1}^T x < g_{k+1}) { ... }
      x = Ukx + vk;
    }
    x = Uk-1x + vk-1;
  }
  ...
  x = U1x + v1;
}

```



### Number of iterations

$$N = \sum_{i_1=0}^{n_1(x_{0,1})} \sum_{i_2=0}^{n_2(x_{0,2})} \dots \sum_{i_{r-1}=0}^{n_{r-1}(x_{0,r-1})} n_r(x_{0,r})$$



### Program analysis

$$W = N \Big|_{p=1}$$

$$D = N \Big|_{p \rightarrow \infty}$$

# Static Loop Counting: Case studies

**CG – conjugate gradient**

$$N \approx k_1 \left\lceil \frac{m}{p} \right\rceil + k_2 \sqrt{\left\lceil \frac{m}{p} \right\rceil} + k_3 \log_2 \sqrt{p}$$

**IS – integer sort**

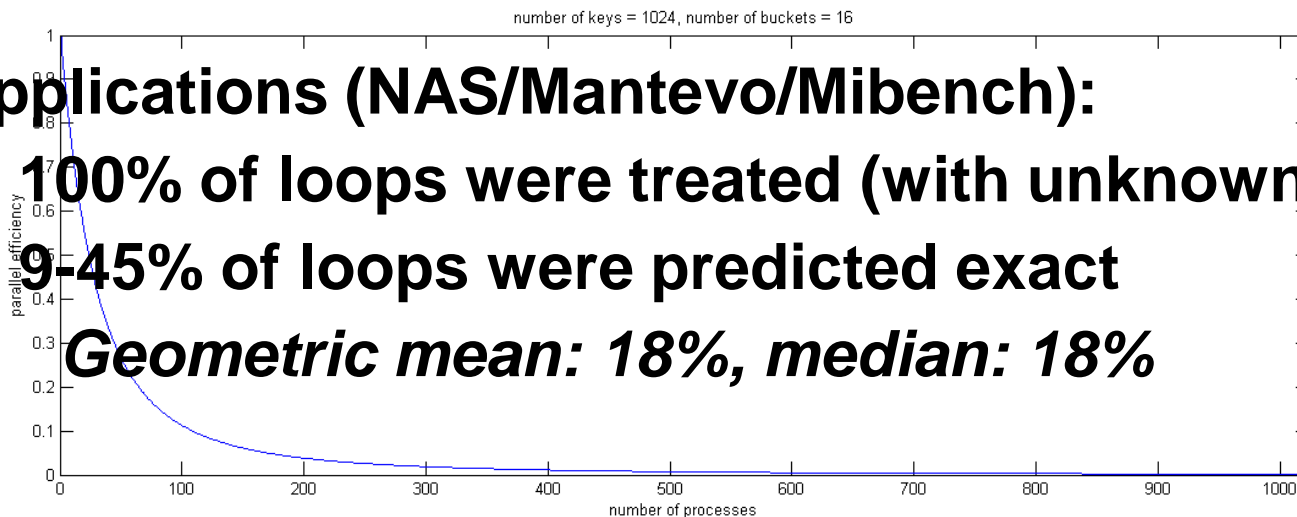
$$D = T_\infty \approx \left( 3 \left\lceil \frac{m}{p} \right\rceil + 2 \left\lceil \frac{m}{p} \right\rceil + p + u_1 + u_2 \right)$$

$$E_p = \frac{D = T_\infty = \infty}{k_4} \frac{1}{p \left( k_1 \left\lceil \frac{m}{p} \right\rceil + k_2 \sqrt{\left\lceil \frac{m}{p} \right\rceil} + k_3 \log_2 \sqrt{p} \right)}$$

**15 applications (NAS/Mantevo/Mibench):**

- **100% of loops were treated (with unknowns)**
- **9-45% of loops were predicted exact**

**Geometric mean: 18%, median: 18%**



# When Static doesn't work – PMNF!

$$f(p) = \prod_{k=1}^n c_k \times p^{i_k} \times \log_2^{j_k}(p)$$

$$\begin{array}{l} n \hat{=} \mathbb{N} \\ i_k \hat{=} I \\ j_k \hat{=} J \\ I, J \hat{=} \mathbb{Q} \end{array}$$

$$n = 1$$

$$I = \{0, 1, 2\}$$

$$J = \{0, 1\}$$

$c_1$	$c_1 \times \log(p)$
$c_1 \times p$	$c_1 \times p \times \log(p)$
$c_1 \times p^2$	$c_1 \times p^2 \times \log(p)$

# Application Scalability – PMNF!

$$f(p) = \prod_{k=1}^n c_k \times p^{i_k} \times \log_2^{j_k}(p)$$

 $n \in \mathbb{N}$ 
 $i_k \in I$ 
 $n = 2$ 
 $I = \{0, 1, 2\}$ 
 $J = \{0, 1\}$ 

$c_1 + c_2 \times p$

$c_1 + c_2 \times p^2$

$c_1 + c_2 \times \log(p)$

$c_1 + c_2 \times p \times \log(p)$

$c_1 + c_2 \times p^2 \times \log(p)$

$c_1 \cdot \log(p) + c_2 \cdot p$

$c_1 \cdot \log(p) + c_2 \cdot p \cdot \log(p)$

$c_1 \cdot \log(p) + c_2 \cdot p^2$

$c_1 \cdot \log(p) + c_2 \cdot p^2 \cdot \log(p)$

$c_1 \cdot p + c_2 \cdot p \cdot \log(p)$

$c_1 \cdot p + c_2 \cdot p^2$

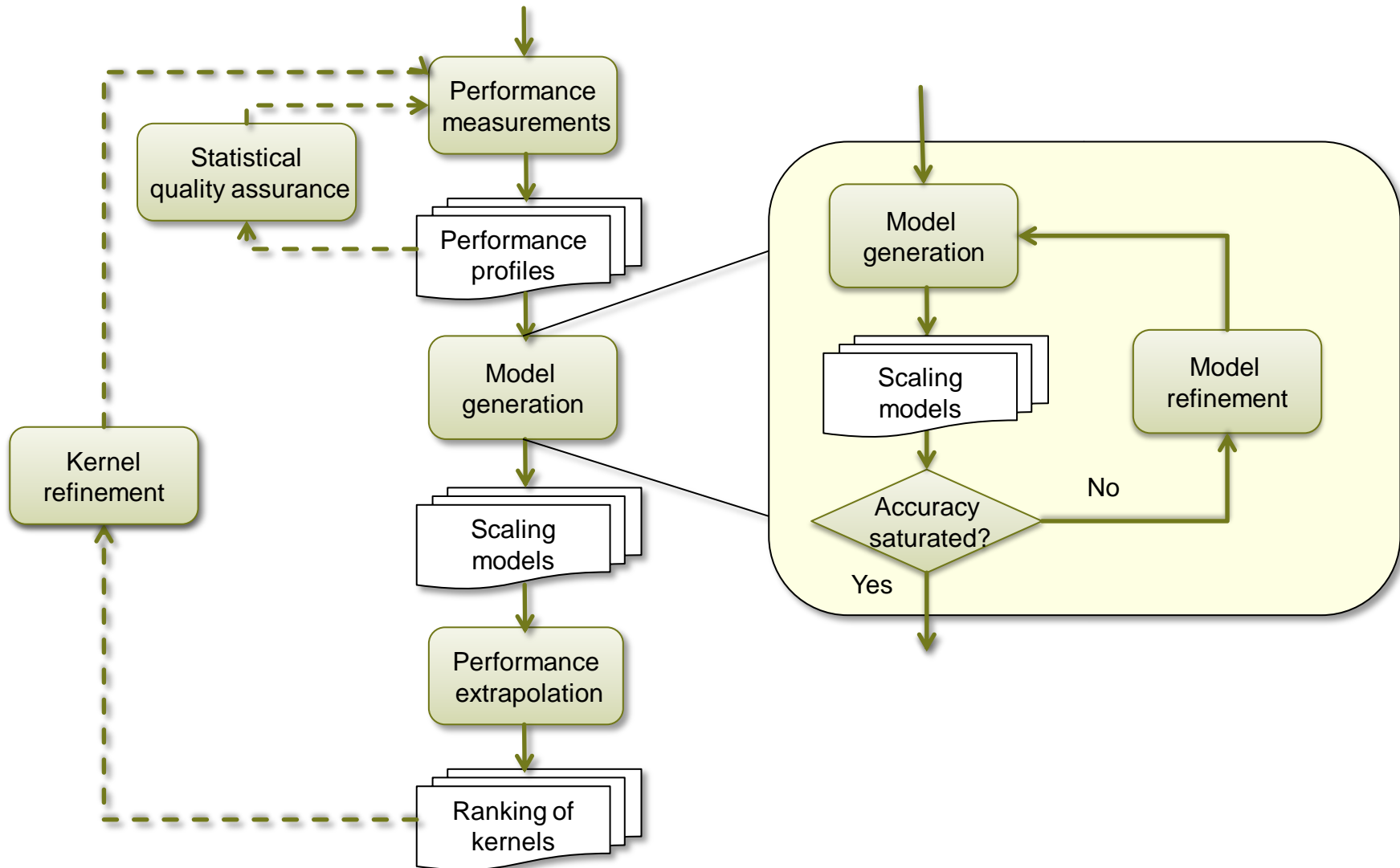
$c_1 \cdot p + c_2 \cdot p^2 \cdot \log(p)$

$c_1 \cdot p \cdot \log(p) + c_2 \cdot p^2$

$c_1 \cdot p \cdot \log(p) + c_2 \cdot p^2 \cdot \log(p)$

$c_1 \cdot p^2 + c_2 \cdot p^2 \cdot \log(p)$

# Our automated generation workflow





# Model refinement

